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None	4800-400	UNIT ASSEMBLY INTERCONNECTING DIAGRAM
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	4800-071	BASIC OSCILLATOR SCHEMATIC
A2	4800-701	SINGLE PHASE to MULTIPHASE CONVERTER ASSEMBLY
	4800-072	SINGLE PHASE to MULTIPHASE CONVERTER SCHEMATIC
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## CALIBRATION PROCEDURE



# SPECIFICATIONS

MODEL	MODEL	MODEL
800T-20/20K-1-2φ	800T-20/20K-1-3φ*	800T-20/20K-1-1φ

NUMBER OF PHASES:

Single Phase

Three Phase

Two Phase

PHASE SEQUENCE:

Not Applicable

ABC Sequence

CA Sequence

PHASE ACCURACY AT 25°C:

Not Applicable

±2 degrees\*\*

±2 degrees\*\* maximum

## SPECIFICATIONS COMMON TO ALL MODELS

FREQUENCY RANGE:

20 Hz to 20 KHz in three decade ranges.

FREQUENCY ACCURACY AT 25°C:

±1.0 per cent of reading.

FREQUENCY TEMPERATURE COEFFICIENT:

±0.02 per cent/°C maximum.

FREQUENCY RESPONSE:

±1.0 per cent from 20 Hz to 2.0 KHz and within ±3 per cent to 20KHz referenced to the 400Hz output.

OUTPUT AMPLITUDE:

5.000 volts rms ±50 millivolts at 400Hz for single phase model. 0 to greater than 5 volts rms for multiphase models.

AMPLITUDE STABILITY:

±0.25 per cent for 24 hours at 25°C; ±0.02 per cent/°C maximum.

(after one hour warm up)





TOTAL  
HARMONIC  
DISTORTION: 0.15 per cent maximum from 45 Hz to 5 KHz and increasing to .25 per cent maximum at 20 KHz.

INPUT  
POWER:  $\pm 25$  volts DC at approximately  $\pm 30$  milliamperes available from power amplifier.

OPERATING  
TEMPERATURE  
RANGE:

0 to 55°C for all versions.

DIMENSIONS:

3 1/2" high x 8" wide x 7" deep for mounting to a California Instruments Solid State Invertron<sup>®</sup>.

FRONT PANEL  
FINISH:

Grey 26440, Federal Standard 595 with black silk screened lettering.

All specifications apply when tested in accordance with the test procedures given in this manual.

\* Model 800T-20/20K-1-3 $\phi$ D will provide a three phase output when used with two power amplifiers connected in the open delta configuration.

\*\* Option F. These oscillators are available with  $\pm 1.0$  degree phase accuracy and improved amplitude tracking upon special order. Consult the factory.



## GENERAL DESCRIPTION

### 1.1 INTRODUCTION

This instruction manual contains information on the installation, operation, calibration and maintenance of the California Instruments 800T Series Variable Frequency Oscillators. A detailed theory of operation is provided as an aid to maintenance personnel. A complete parts listing, schematics and component location drawings are also supplied.

### 1.2 MODEL NUMBER DESCRIPTION

All oscillators in the 800T Series have a four place model number which describes the following features: the oscillator series, the output frequency range of the oscillator, the frequency tolerance of the oscillator, and the number of phases of the oscillator. In addition, a "D" is placed after the model number if the oscillator is to be used in the three phase, open delta configuration. The following examples illustrate this numbering system.

#### Example 1: 800T-20/20K-1-3φ

This is a variable frequency, three phase oscillator that covers the range from 20 Hz to 20 KHz with a specified frequency accuracy of  $\pm 1.0$  per cent.

#### Example 2: 800T-20/20K-1-3φD

This is a variable frequency, three phase oscillator for use in the open delta configuration that covers the range from 20 Hz to 20 KHz with a specified frequency accuracy of  $\pm 1.0$  per cent.

#### Example 3: 800T-20/20KHz-1-1φ

This is a variable frequency, single phase oscillator that covers the range from 20 Hz to 20 KHz with a specified frequency accuracy of  $\pm 1.0$  per cent.

### 1.3 GENERAL DESCRIPTION

The California Instruments 800T Series of Variable Frequency Oscillator provide a pure sine wave output for driving the California Instruments Solid State Inverters. These oscillators incorporate a unique amplitude regulator circuit which prevents overshoot as the frequency of the oscillator is varied or as the decade range of the oscillator is changed. This prevents possible damage to a sensitive load connected to the output of the inverter as a result of excessive output voltage amplitude. The multi-phase versions of the 800T Series Oscillators are used to drive two or more Solid State Inverters connected so as to provide either two phase or three phase power.



on contact may result if personnel fail to observe safety precautions. Do not touch electronic circuits when power is applied. Avoid contact with connector pins C and D of the plug in oscillator, the primary power circuits, and output circuits of the associated Invertron. If oscillator is tested and adjusted with Invertron.

## DEATH

Voltages up to 500 VAC are available in certain associated Invertrons. This equipment generates potentially lethal voltages.

HIGH VOLTAGE (115 AC)

## WARNING

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The following equipment is available for use with the 800T Series Variable Frequency Oscillators, 1,4,1 4800-703 Extender Assembly. This assembly allows the oscillator to be tested and adjusted outside of the associated Solid State Invertron.

1:4 ACCESSORY EQUIPMENT



# INSTALLATION AND OPERATION

## 2.1 UNPACKING

Individual oscillators are shipped in a cardboard container with protective inner packing. Do not destroy this packing container until the unit has been inspected for possible damage in shipment.

## 2.2 POWER REQUIREMENTS

The California Instruments 800T Series Variable Frequency Oscillators operate from plus (+) and minus (-) 25 volts DC at 0.030 amperes. This power is normally obtained from the associated Solid State Invertron.

## 2.3 FUSE REQUIREMENTS

No separate fuse is required with the California Instruments 800T Series Variable Frequency Oscillators.

## 2.4 ACCEPTANCE TEST PROCEDURE

Inspect the unit for any possible shipping damage immediately upon receipt. If damage is evident, notify the carrier. DO NOT return an instrument to the factory without prior approval. If the unit appears in good condition, perform the following:

Mount the oscillator in the appropriate California Instruments Invertron or otherwise apply  $\pm 25$  volts DC power to the unit.

## 2.4.1 MODEL 800T-20/20K-1-1 $\phi$ SINGLE PHASE OSCILLATOR

### 2.4.1.1

Connect an oscilloscope, a differential voltmeter and a counter to the output of the oscillator (Pin 1 of the printed circuit connector is the output "low" and Pin 2 of the printed circuit connector is the output "high" as given in Section 2.5 of this instruction manual). Set the frequency of the 800T Series Oscillator to 400 Hz and check that the output voltage from the oscillator is 5.00 volts rms  $\pm 0.05$  volts and that the output wave form is a pure sine wave with no clipping or other distortion. The counter should indicate within  $\pm 1.0$  per cent of 400 Hz.

### 2.4.1.2

Vary the frequency of the oscillator from 20 Hz to 2.0 KHz and check that the oscillator output amplitude remains within  $\pm 1.0$  per cent of its value at 400 Hz. Vary the frequency of the oscillator from 2.0 KHz to 20 KHz and check that the oscillator output amplitude remains within  $\pm 3$  per cent of its value at 400 Hz. Check that the frequency of the oscillator remains within  $\pm 1$  per cent of the dial setting and that the output wave form is a pure sine wave with no clipping or other distortion.





- 2.4.1.3 If further acceptance testing of this oscillator is required, refer to the complete test procedure given in Section 4.1 of this instruction manual.
- 2.4.2 MODEL 800T-20/20K-1-2 $\phi$  TWO PHASE OSCILLATOR
- 2.4.2.1 Rotate the AMPLITUDE control on the two phase oscillator to a fully clockwise position and repeat steps 2.4.1.1 and 2.4.1.2 for both the "A" phase and "C" phase outputs. In this case, the output voltage should be greater than 5 volts rms and the amplitude of both phases should be within 2 per cent of each other.
- 2.4.2.2 Connect a differential phase meter to the "A" phase and "C" phase outputs and check that "A" phase lags "C" phase by  $90^\circ \pm 2^\circ$  over the frequency range from 20 Hz to 20 KHz.
- 2.4.2.3 If further acceptance testing of this oscillator is required, refer to the complete test procedure given in Section 4.1 of this instruction manual.
- 2.4.3 MODEL 800T-20/20K-1-3 $\phi$ D THREE PHASE OSCILLATOR
- 2.4.3.1 Repeat step 2.4.2 except check that the phase "A" output lags the phase "C" output by  $60^\circ \pm 2^\circ$ .
- 2.4.4 MODEL 800T-20/20K-1-3 $\phi$  THREE PHASE OSCILLATOR
- 2.4.4.1 Rotate the AMPLITUDE control on the three phase oscillator to a fully clockwise position and repeat steps 2.4.1.1 and 2.4.1.2 for all three phase outputs. In this case, the output voltage should be greater than 5 volts rms and the amplitude of all three phases should be within 2 per cent of each other.
- 2.4.4.2 Connect a differential phase meter to the "A" phase and "C" phase outputs and check that the "A" phase output lags the "C" phase output by  $120^\circ \pm 2^\circ$  over the frequency range from 20 Hz to 20 KHz. Connect a differential phase meter to the "A" phase and "B" phase outputs and check that the "A" phase output leads the "B" phase output by  $120^\circ \pm 2^\circ$  over the frequency range from 20 Hz to 20 KHz.
- 2.4.4.3 If further acceptance testing of this oscillator is required, refer to the complete test procedure given in Section 4.1 of this instruction manual.



2.5 MECHANICAL INSTALLATION and WIRING

The 800T Series Variable Frequency Oscillators fit directly into the California Instruments Solid State Invertron® Series of power amplifiers. All power for the oscillator, as well as signal output, is coupled through the printed circuit connector at the rear of the oscillator. The following table lists the voltage and applicable connector pins.

Pin	Function
1	Output signal low
2	Output signal high
3	Power ground
4	+25 volt input
5	-25 volt input
6	Output signal low
7	Output signal high
8	Output signal low
9	Output signal high
10	External sync input
C	115 volts AC line high
D	115 volts AC line low

Phase A }  
 Phase B }  
 Phase C }

2.6 OPERATING CONTROLS

The single phase version of the 800T Series Oscillator only has an "A" phase output. The two phase version and the three phase open delta version of the 800T Series Oscillator has an "A" and "C" phase output while the standard three phase version of this oscillator has all three outputs. The external sync input and 115 volt AC inputs are not used for the standard version of this oscillator.

All operating controls for the 800T-20/20K Series Oscillators are located on the front panel of the oscillator.

2.6.1

The decade range of the oscillator is determined by the three-position rotary switch identified on the front panel as FREQUENCY-Hz. The oscillator covers the range from 20 Hz to 200 Hz with this control in the counter clockwise position; 200 Hz to 2 KHz with this control in the center position; and 2 KHz to 20 KHz with this control in the clockwise position.

2.6.2

The large CALIBRATED DIAL located on the left side of the oscillator front panel is used to determine the numerical value of the frequency of the oscillator. Rotate this dial until the desired frequency is located adjacent to the hair line calibration mark just above the top of the dial.



**CAUTION**

DO NOT USE OSCILLATOR  
 IN REGION FROM 20 Hz TO  
 40 Hz UNLESS OUTPUT OF  
 INVERTRON IS REDUCED  
 TO AT LEAST ONE HALF  
 VOLTAGE.

**CAUTION**

REMOVE POWER FROM THE  
 SOLID STATE INVERTRON  
 BEFORE REMOVING OR IN-  
 SERTING PLUG-IN  
 OSCILLATOR.

2.6.3 Single phase versions of this oscillator have no front panel AMPLITUDE control. In this case, the GAIN control on the front panel of the Invertron is used to control the output amplitude of the power source.

2.6.4 Multi-phase versions of this oscillator have an AMP-LITUDE ADJUST control which is used to vary the output voltage level of all phases of the oscillator from 0 volts to greater than 5.0 volts rms as the control is rotated in the clockwise direction.

In a multi-phase power source, the GAIN control on each of the associated Invertrons is used as a trim control to adjust each of the output leg voltages so that they are precisely equal to each other.



## 3.1 DESCRIPTION OF SINGLE PHASE OSCILLATOR

A block diagram for the basic single phase oscillator in the 800T Series is given in Figure 2. A schematic diagram for the oscillator printed circuit board (designated as the A1 Assembly) is given in drawing D4800-071 and a schematic diagram for the frequency determining printed circuit boards (designated as the A3 Assembly) is given in drawing D4800-073. An inner connecting wiring diagram is given in drawing D4800-070 for single phase, two phase and three phase versions of this oscillator.

The frequency determining elements are arranged in a Wien bridge configuration which is connected as a positive feedback loop around an operational amplifier as illustrated in Figure 2 and fully described in Section 3.1.1 through 3.1.6 of this instruction manual. The frequency of oscillation  $f_0$  is given by the equation,

$$f_0 = \frac{1}{2\pi RC}$$

where R is the total resistance in the positive feedback path and C is the total capacitance in the positive feedback path. At the frequency of oscillation  $f_0$ , the positive feedback loop has a transmission of 0.333 and a zero phase shift. The negative feedback loop must adjust the closed loop gain of the operational amplifier A1IC2 to exactly 3.00 in order for the Wien bridge oscillator to produce a pure sine wave output with a minimum of distortion. This is accomplished by means of a servo feedback loop consisting of the amplitude regulator element, a differential error integrator, a DC reference voltage and a peak detector connected as shown in Figure 2.

The output impedance of the amplitude regulator element varies as a function of its DC input voltage and, in this way, controls the closed loop gain of the operational amplifier A1IC2. The amplitude regulator is driven from the DC output of the error integrator.

The error integrator provides a DC output proportional to the difference between the value of the DC reference voltage and the output of the peak detector. The peak detector provides a DC output almost equal to the peak value of the sine wave at the output of the Wien bridge oscillator. If the sine wave output of the Wien bridge oscillator increases for any reason, then the DC output of the peak detector also increases a like amount. This, in turn, drives the output of the error integrator in a negative direction. This negative going bias at the input to the amplitude regulator causes its output impedance to increase, thereby, reducing the closed loop gain of the integrated circuit amplifier, A1IC2. This, in turn, causes the output amplitude of the Wien bridge oscillator to drop and return to its original value.





FIGURE 2. Block Diagram of Basic Wien Bridge Oscillator Employed in 800T Series Oscillator.

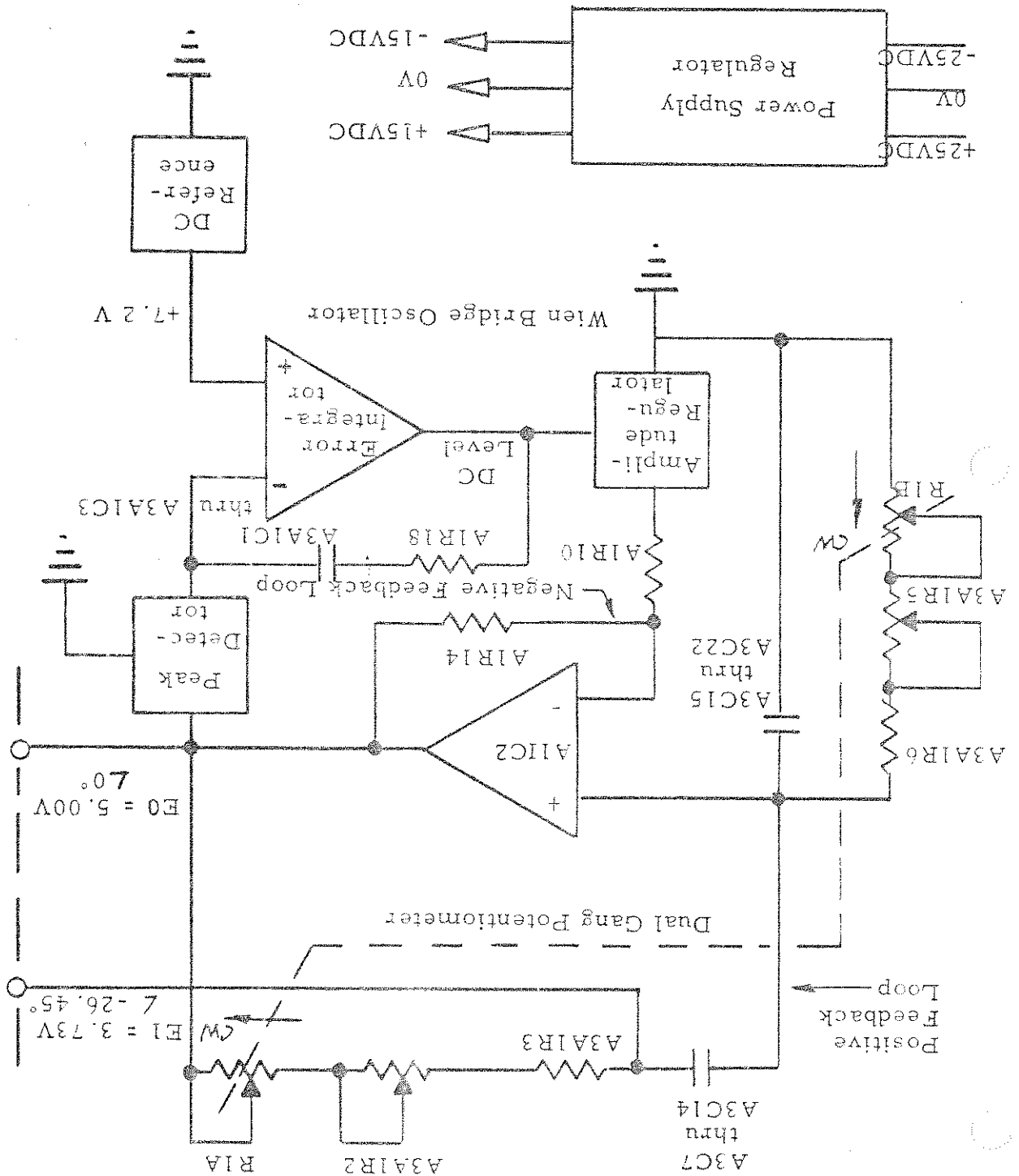
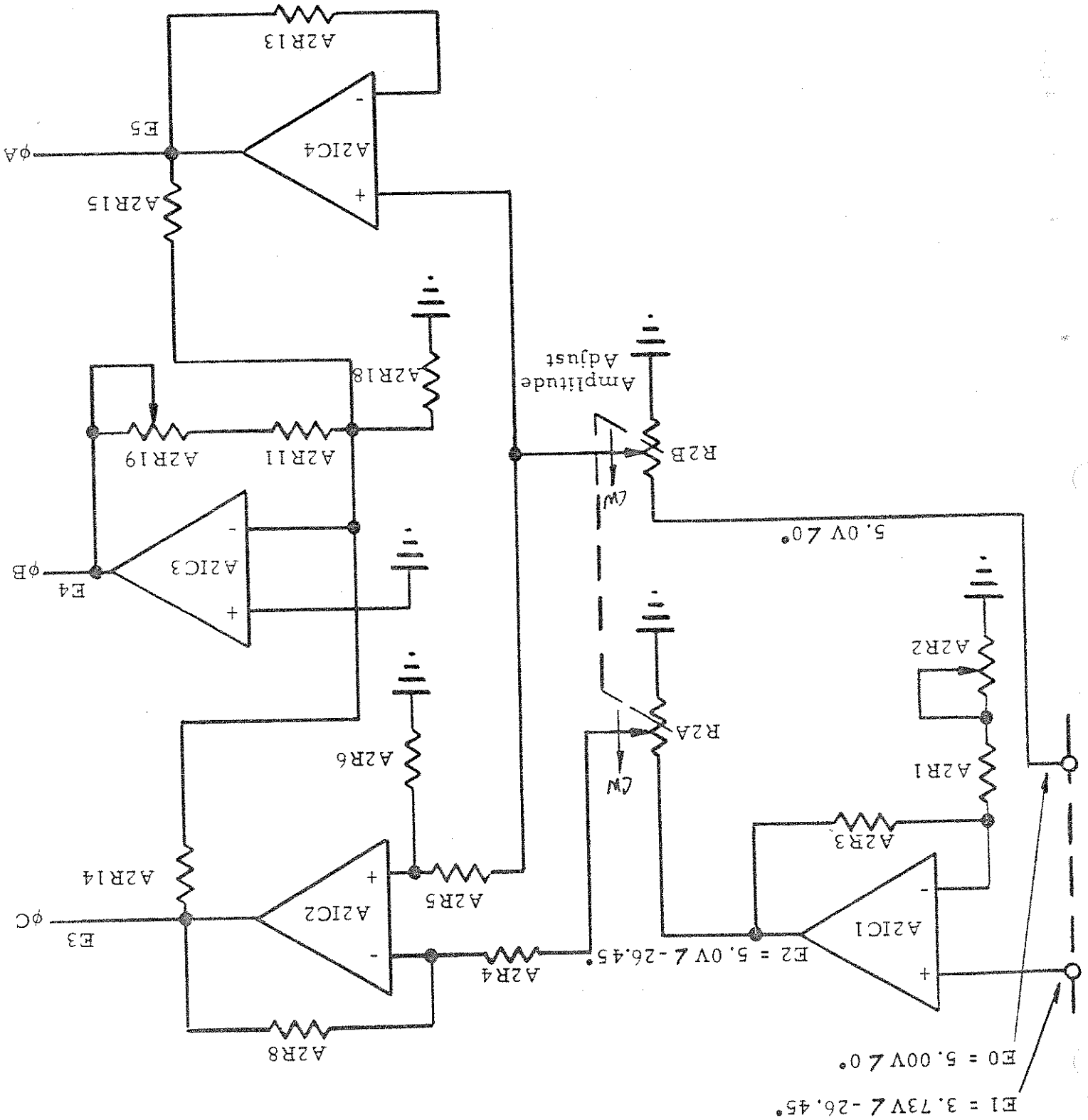




FIGURE 3. Block Diagram of Single to Multiphase Converter employed in 800T Series Oscillator.





### 3.1.1 RANGE SWITCH CIRCUITRY

A schematic diagram for the range switch circuitry is shown in drawing D4800-073. These circuits include the frequency determining elements of the Wien bridge oscillator as well as time constant determining elements for both the peak detector and the error integrator.

Capacitors A3C12, A3C13, A3C14, A3C20, A3C21 and A3C22 are switched into the Wien bridge oscillator on the 20 Hz to 200 Hz range. Capacitors A3C9, A3C10, A3C11, A3C17, A3C18 and A3C19 are switched into the Wien bridge oscillator on the 200 Hz to 2 KHz range. Capacitors A3C7, A3C8, A3C15 and A3C16 are switched into the oscillator circuit on 2 KHz to 20 KHz range. Capacitors A3C7, A3C10, A3C12, A3C15, A3C18 and A3C20 are used to compensate for the temperature coefficient of A3C8, A3C11, A3C13, A3C16, A3C19 and A3C21, respectively. Capacitors A3C7, A3C9, A3C14, A3C15, A3C17 and A3C22 are determined at the factory so that the low frequency limits of each range are in exact decade steps with respect to each other.

Potentiometers A3A1R1, A3A1R2, A3A1R4 and A3A1R5 are switched with the range determining switch. Their function is to set the electrical range limits of the front panel precision potentiometer R1A, R1B so that the CALIBRATED DIAL covers an exact frequency decade as it is rotated from the "2" indication to the "20" indication.

Capacitors A3A1C1, A3A1C2 and A3A1C3 are switched with the range determining capacitors. Their function is to determine the time constant of the error integrator.

Capacitor A3A1C4, A3A1C5 and A1A3C6 are also switched with the range determining capacitors. The function of these capacitors is to determine the time constant of the filter associated with the peak detector.

### 3.1.2 AMPLITUDE REGULATOR ELEMENT

The amplitude regulator element consists of mosfet A1Q3 and associated components given in drawing D4800-071. The output impedance of the mosfet varies from approximately 125 ohms with +10 volts gate bias to 250 ohms with 0 volts gate bias and to 100 megohms with -4 volts gate bias. Under normal operating conditions, the gate bias on A1Q3 is between +10 and +2 volts DC which provides a mosfet output impedance between 125 ohms and 200 ohms. Potentiometer A1R11, which is in series with the output of the amplitude regulator, is adjusted to a region which provides both reliable starting of the oscillator over the full 20 Hz to 20 KHz frequency range and an output distortion of less than 0.15 per cent over the frequency range from 45 Hz to 5 KHz, when measured at eyelet A1-14.



### 3.1.3 DIFFERENTIAL ERROR INTEGRATOR

The differential error integrator consists of integrated circuit A11C3 and associated components connected as shown in drawing D4800-071. Capacitors A1C3, A1C4 and resistor A1R15 form a pair of local feedback loops which are used to provide an adequate high frequency stability margin when A11C3 is used as an operational integrator. The integrating capacitor is selected from A3A1C1, A3A1C2 and A3A1C3 as determined by the range selector switch A3S1. The DC reference voltage is applied to the non-inverting input of the error integrator and the output of the peak detector is applied to the inverting input of the error integrator. Therefore, when the peak detector output voltage exceeds the DC reference voltage, the output of the error integrator will be driven in the negative direction.

### 3.1.4 PEAK DETECTOR

The peak detector consists of diode A1C7 and a filter capacitor selected by range switch A3S1. The DC output voltage of the peak detector is approximately 200 millivolts below the peak value of the sine wave output from the Wien bridge oscillator. This corresponds to a peak current of approximately 200 micro amperes through the detector diode.

### 3.1.5 DC REFERENCE

The DC reference consists of transistor A1Q1, zener diode A1C1, integrated circuit A11C1 and associated components as given in drawing D4800-071. Transistor A1Q1 is connected as a constant current source and provides power to the zener reference diode A1C1. The +6.2 volt DC output of this diode is amplified to approximately 7.2 volts by A11C1 connected as a voltage amplifier. Resistor A1R1 is selected at the factory so that the output of the single phase oscillator is 5.00 volts rms  $\pm 0.05$  volts.

### 3.1.6 REGULATED DC POWER SUPPLIES

The DC regulated power supply consists of transistors A1O2, A2Q4 and associated components as also given in drawing D4800-071. Transistor A1Q2 is connected in the emitter follower configuration and supplies  $\pm 1.5$  volt. The ripple on this regulated power supply is less than 20 millivolts peak-to-peak. Transistor A1Q4 is also connected in the emitter follower configuration and supplies  $\pm 1.5$  volt with less than 20 millivolts peak-to-peak ripple.





3.2 GENERAL DESCRIPTION OF SINGLE PHASE TO MULTI-PHASE CONVERTER

A block diagram for the single phase to multi-phase converter is given in Figure 3. This circuit employs an analog computer technique of generating the second and third phases of the multi-phase output. This technique requires no additional tuned circuits and provides the required phase shift independent of oscillator frequency.

The operation of this circuit is dependent on the signal  $E_1$  which is available at the junction of the resistive and capacitive elements in the positive feedback loop of the Wien bridge oscillator. A detailed analysis of the basic Wien bridge circuit configuration reveals that the voltage  $E_1$  at the frequency of the oscillation is given by:

$$E_1 = 0.746 E_0 \angle -26.45^\circ$$

Therefore, as the frequency of the Wien bridge oscillator changes, with changes in  $R_1A$  and  $R_1B$ , the voltage  $E_1$  remains fixed in amplitude and phase shift with respect to the basic oscillator output voltage,  $E_0$ .

The voltages,  $E_0$  and  $E_1$ , are then processed through a sequence of operational amplifiers to provide the phase "A", phase "B" and phase "C" outputs. The vector diagram below describes how this is accomplished in graphical terms with the AMPLITUDE potentiometer  $R_2$  in the fully clockwise position.

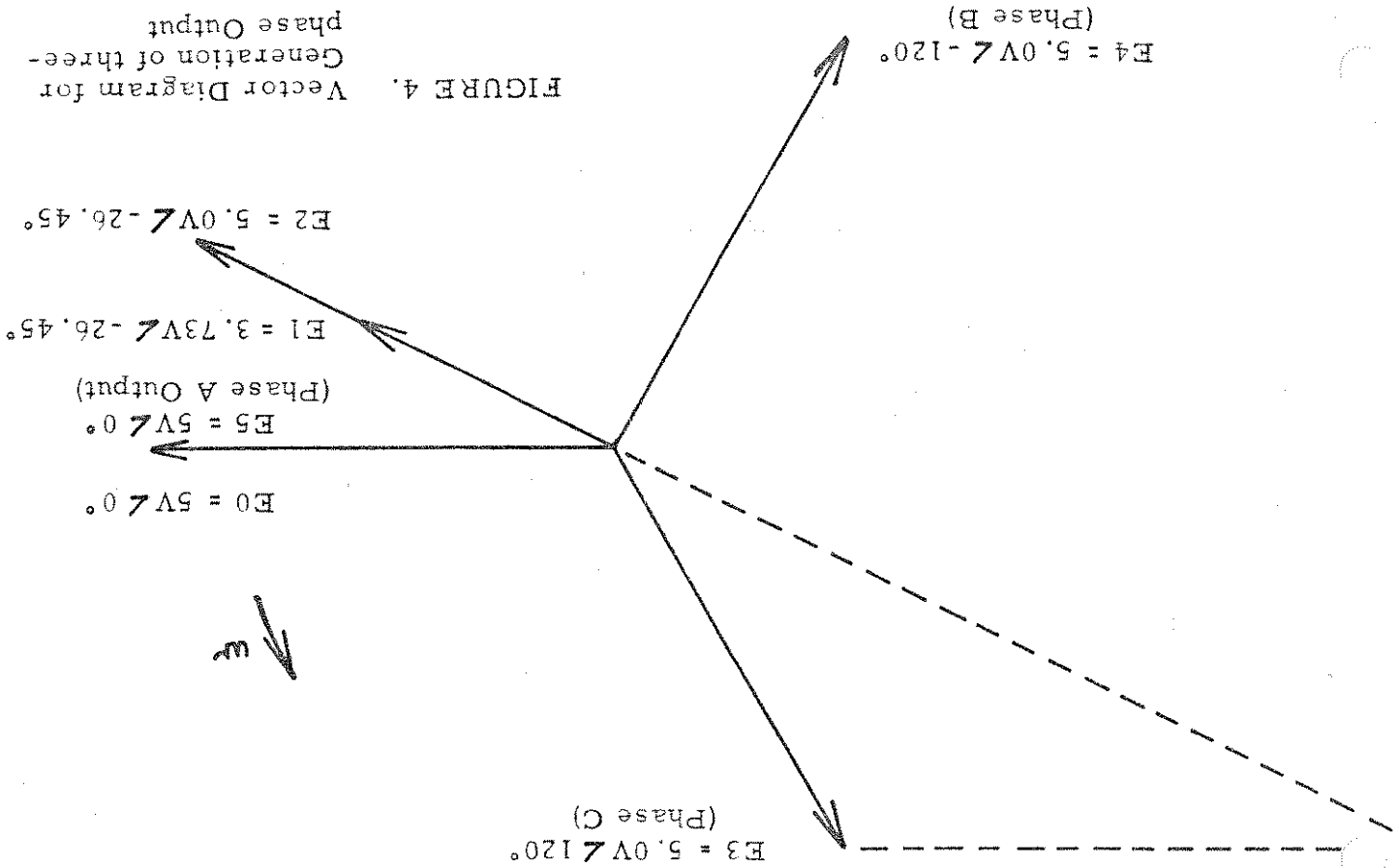


FIGURE 4. Vector Diagram for Generation of three-phase Output



# WARNING

HIGH VOLTAGE (115 AC)

Voltages up to 500 VAC are available in certain associated Invertrons.<sup>®</sup>  
This equipment generates potentially lethal voltages.

# DEATH

on contact may result if personnel fail to observe safety precautions.  
Do not touch electronic circuits when power is applied. Avoid contact  
with connector pins C and D of the plug in oscillator, the primary  
power circuits, and output circuits of the associated Invertron if os-  
cillator is tested and adjusted with Invertron .



## TEST PROCEDURE

### 4.1 GENERAL

The following test procedure may be performed on a routine basis to insure that the 800T-20/20 KHz Variable Frequency Oscillator remains within the specified performance limits. This test procedure is divided into several sections. These are:

- 1) Accuracy of the frequency dial calibration and frequency stability
- 2) Output voltage and amplitude stability
- 3) Harmonic distortion
- 4) Phase accuracy

### 4.2 TEST EQUIPMENT

The following test equipment is required to perform the test procedure:

- a) Oscilloscope, Tektronix 531A with general purpose plug-in or equivalent
- b) California Instruments Power Source or two H.P. 721A DC Power Supplies or equivalent
- c) Distortion Analyzer, H.P. H02-330B or equivalent
- d) Differential Voltmeter, Fluke 883A or equivalent
- e) Frequency Meter, H.P. 523 CR or equivalent
- f) Phase Meter, Dranetz Model 301 or equivalent

### 4.3 ACCURACY OF THE FREQUENCY DIAL CALIBRATION and FREQUENCY STABILITY

4.3.1 Connect the variable frequency oscillator as shown in Figure 5 and allow the unit to warm up for one hour in a 25°C environment. The output voltage should be approximately 5 volts rms. Adjust the AMPLITUDE control on the multiphase oscillators, as required.

4.3.2 Measure and record the phase "A" output frequency and the CALIBRATED DIAL setting over the range from 20 Hz to 20 KHz. The measured frequency should agree with the CALIBRATED DIAL setting within ±1.0 per cent. Refer to the CALIBRATION PROCEDURE given in Section 5.3 through 5.7 of this manual if problems are encountered.

4.3.3 Adjust the frequency of the oscillator to exactly 400 Hz with the CALIBRATED DIAL in a 25°C environment. Increase the ambient temperature to 50°C and allow the



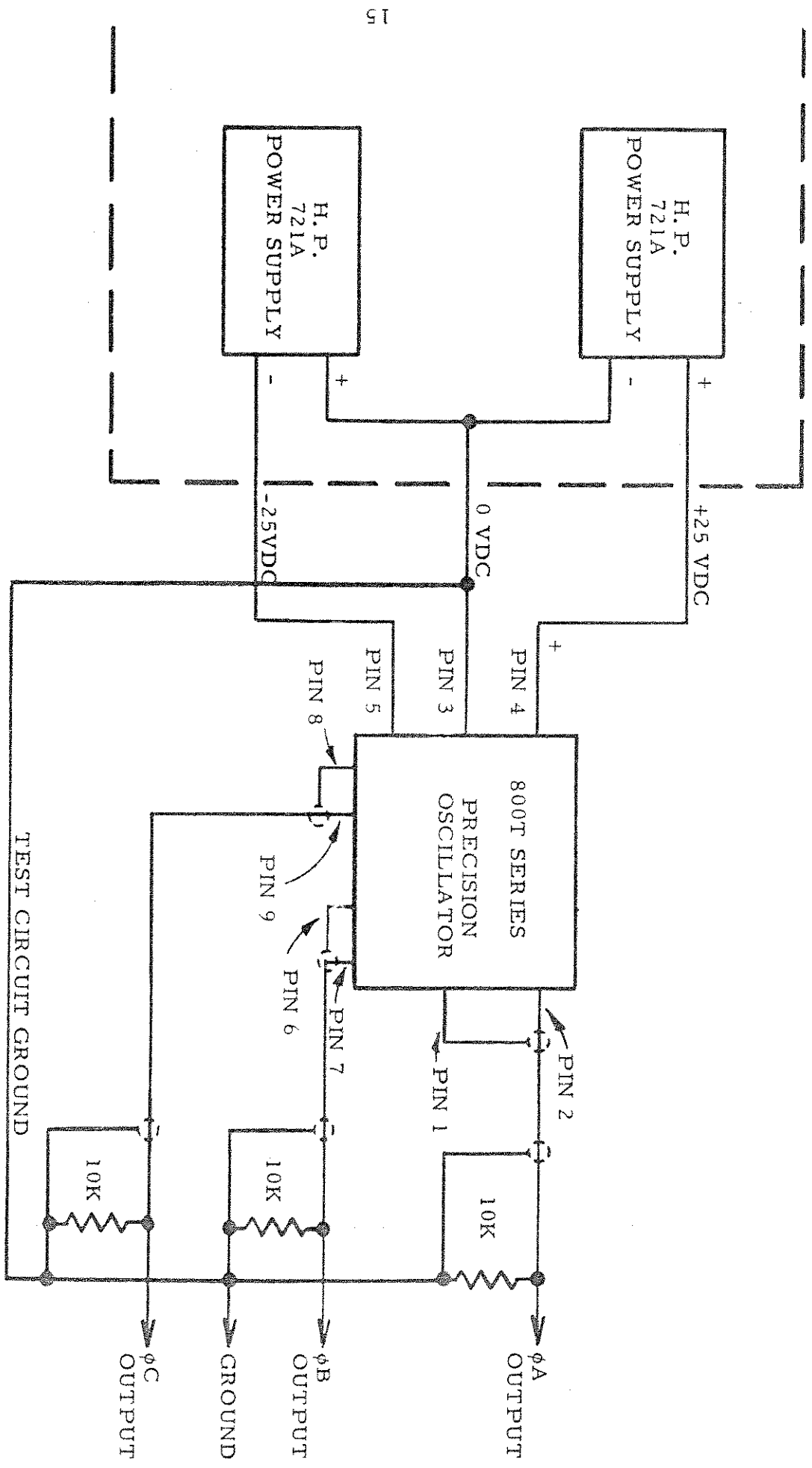


FIGURE 5. Test Circuit for 800T Series Variable Frequency Oscillators





- oscillator to stabilize for one hour. The output frequency should change less than  $\pm 0.5$  per cent from that indicated at  $25^{\circ}\text{C}$ .
- 4.3.4 Step 4.3.3 may be repeated at other oscillator frequencies as required.
- 4.4 OUTPUT VOLTAGE AND AMPLITUDE STABILITY
- 4.4.1 Connect the variable frequency oscillator as shown in Figure 5 and allow the unit to warm up for one hour in a  $25^{\circ}\text{C}$  environment. Set the frequency of the oscillator to 400 Hz. In the case of a multiphase oscillator, rotate the AMPLITUDE control fully clockwise.
- 4.4.2 The output of the oscillator should be measured with differential voltmeter connected to the phase "A" output (Pin 1 and Pin 2 of the printed circuit connector). The output voltage should be 5.0 volts rms  $\pm 0.05$  volt rms for single phase versions of this oscillator and greater than 5.0 volts rms for multiphase versions of this oscillator. Record the exact value of the output voltage.
- 4.4.3 Measure the phase "A" output voltage in 15 minute increments for two hours. The change in output voltage must not exceed  $\pm 12.5$  millivolts rms. This test may be continued for 24 hours if required. Maintain the ambient temperature at  $25^{\circ}\text{C}$  for this test.
- 4.4.4 Increase the ambient temperature to  $50^{\circ}\text{C}$  and allow the unit to stabilize for one hour. The output should change less than  $\pm 25$  millivolts rms.
- 4.4.5 Steps 4.4.3 and 4.4.4 may be repeated for other frequencies in the 20 Hz to 20 KHz range, if desired.
- 4.4.6 Vary the oscillator frequency from 20 Hz to 20 KHz and check that the output amplitude remains within  $\pm 1$  per cent band from 20 Hz to 2.0 KHz and within  $\pm 3$  per cent band from 20 Hz to 20 KHz referenced to the output at 400 Hz. If problems are encountered, refer to Section 5.8 of this instruction manual.
- 4.4.7 For multiphase versions of this oscillator, repeat step 4.4.6 for both the phase "B" and phase "C" outputs. If problems are encountered, refer to Section 5.10 through 5.12 of this instruction manual.
- 4.4.8 For multiphase versions of this oscillator, all output voltages must have the same amplitude within 2 per cent over the 20 Hz to 20 KHz frequency range.



## HARMONIC DISTORTION

4.5

4.5.1 Connect the variable frequency oscillator as shown in Figure 5 and allow the unit to warm up for a few minutes. In the case of multiphase oscillators, turn the AMPLITUDE control fully clockwise.

4.5.2 Connect the distortion analyzer between the high output of phase "A" and the ground. The distortion should be less than 0.15 per cent over the frequency range from 45 Hz to 5 KHz and less than 0.25 per cent from 5 KHz to 20 KHz. If problems are encountered, refer to Section 5.9 of this instruction manual.

4.5.3 If the oscillator is a multiphase oscillator, then repeat 4.5.2 for the other phase or phases. The distortion should not exceed that specified in Section 4.5.2.

## PHASE ACCURACY

4.6

This section of the manual is only applicable to the multiphase oscillators. Connect the oscillator as shown in Figure 5 and allow the unit to warm up for one hour in a 25°C environment.

4.6.1 Rotate the front panel AMPLITUDE control fully clockwise so that the output of phase "A" (Pin 1 and Pin 2 of the printed circuit connector) is greater than 5 volts rms. Set the oscillator frequency to 400 Hz.

4.6.2 Connect a differential phase meter between the phase "A" output and the phase "C" output so that the phase meter reads the phase "C" output referenced to the phase "A" output. The phase meter should indicate  $\pm 60 \pm 2$  degrees for the three phase open delta oscillator;  $\pm 90 \pm 2$  degrees for the two phase oscillator; and  $\pm 120 \pm 2$  degrees for the standard three phase oscillator. Repeat this test over the 20 Hz to 20 KHz frequency range.

4.6.3 For the standard three phase oscillator, measure the phase shift of the phase "B" output referenced to the phase "A" output. This phase shift must be  $-120 \pm 2$  degrees over the 20 Hz to 20 KHz frequency range. If problems are encountered, refer to Section 5.10 through 5.12 of this instruction manual.



CALIBRATION PROCEDURE  
5.1 GENERAL

The following calibration procedure should be followed during initial check out of the oscillator or after any service to the frequency determining circuitry. See Figure 6 for location of internal adjustments. The calibration procedure is divided into several sections and should be performed in this sequence. The sections are:

- 1) Mechanical adjustment procedure for calibrated frequency dial
- 2) Initial calibration procedure for Wien bridge oscillator
- 3) Exact frequency calibration procedure for Wien bridge oscillator on 20 Hz to 200 Hz range
- 4) Exact frequency calibration procedure for Wien bridge oscillator on 200 Hz to 2 KHz range
- 5) Exact frequency calibration procedure for Wien bridge oscillator on 2 KHz to 20 KHz range
- 6) Frequency response adjustments
- 7) Absolute AC output voltage and harmonic distortion
- 8) Phase adjustments for single phase to multiphase converter.

5.2 TEST EQUIPMENT REQUIRED

The same test equipment is required for the calibration procedure as was required for the test procedure in Section 4.2 of this instruction manual.

5.3 MECHANICAL ADJUSTMENT PROCEDURE FOR CALIBRATED FREQUENCY DIAL

- 5.3.1 Rotate the precision potentiometer fully clockwise and then fully counter clockwise, checking that the limit of mechanical rotation agrees with or slightly exceeds the "2" indication and "20" indication on the calibrated dial. The dial stops may be adjusted by a slight amount of filing if required. If an extremely minor mechanical re-alignment is required, it may be possible to readjust the hair line slightly.

5.3.2

Check that the open resistance point on the precision potentiometer R1 is located off the calibrated section of the dial. Check that the resistance of both sections of the precision potentiometer is 500 ohms  $\pm$  5 ohms in the fully counter clockwise position and 9700 ohms  $\pm$  100 ohms in the fully clockwise position when measured between Pin 1 and Pin 2 of both sections of potentiometer R1. The brass coupling may be loosened and the precision potentiometer adjusted slightly, if required.



5.4 INITIAL CALIBRATION PROCEDURE FOR WIEN BRIDGE OSCILLATOR

5.4.1 Connect the variable frequency oscillator as shown in Figure 5 using either the  $\pm 25$  volt power supply in the California Instruments Invertron<sup>®</sup> or a pair of external  $\pm 25$  volt and  $-25$  volt DC power supplies. Each power supply should draw from 15 to 30 milliamperes. Allow the unit to warm up for one half hour. Check that the procedure given in 5.3.1 and 5.3.2 has been performed before proceeding further.

5.4.2 Adjust potentiometers A3A1R1, A3A1R2, A3A1R4 and A3A1R5 so that the arm of each potentiometer measures 120 ohms  $\pm 1.0$  ohm to the open end of the potentiometer. This is best accomplished by connecting an ohmmeter from the arm of the potentiometer to the open end of the potentiometer while the range switch is in a position so as to open the potentiometer under test from the rest of the circuit.

5.4.3 Set the calibrated frequency dial and range switch on the oscillator to 20.0 Hz. Starting with potentiometer A1R11 in a fully clockwise position, rotate this control slowly in the counter clockwise direction until the oscillator output just builds up to full output. Rotating this control too far counter clockwise will cause excessive distortion in the output wave form and/or failure to oscillate properly at 20 KHz.

5.4.4 Vary the frequency of the oscillator from 20 Hz to 20 KHz and check that the oscillator is operating over the full frequency range. If the oscillator does not operate over the full frequency range, set the CALIBRATED DIAL to the upper end of the range that fails to oscillate and adjust the appropriate pair of calibration potentiometers equal amounts but in opposite directions. For example, if the oscillator fails to operate on the 2 KHz to 20 KHz range, set the CALIBRATED DIAL to 20 KHz and adjust A3A1R1 and A3A1R4 equal amounts in opposite directions until the oscillator produces a stable sine wave output.

5.4.5 Vary the frequency of the oscillator from 20 Hz to 20 KHz and check that the oscillator is now working roughly correct and that the frequency of the oscillator is within  $-0.0$  per cent to  $+5$  per cent of the CALIBRATED DIAL setting.

5.5 EXACT FREQUENCY CALIBRATION PROCEDURE FOR WIEN BRIDGE OSCILLATOR ON 20 Hz to 200 Hz RANGE

5.5.1 Check that Section 5.4 of this adjustment procedure has been performed before proceeding to this section of the adjustment procedure.





- 5.5.2 Select the 20 Hz to 200 Hz range and turn the CALIBRATED DIAL fully clockwise. Record the output frequency of the oscillator. Turn the CALIBRATED DIAL fully counter clockwise and record the output frequency. If the ratio of these two frequencies is less than 1.0, rotate A3A1R2 and A3A1R5 clockwise, adjusting both potentiometers an equal amount in the same direction. If the ratio of these two frequencies is greater than 1.0, rotate A3A1R2 and A3A1R5 counter clockwise, adjusting both potentiometers an equal amount in the same direction. If the oscillator stops oscillating during this adjustment procedure, vary A3A1R2 or A3A1R5 somewhat by itself until the oscillation builds up again. If the counter is used in the "period" mode of operation, faster and more accurate results may be obtained.
- 5.5.3 Repeat step 5.5.2 until the frequency ratio is exactly 10 to 1.0  $\pm$  0.1 per cent.
- 5.5.4 Turn the CALIBRATED DIAL fully clockwise and select equal value capacitors for A3C14 and A3C22 so that the output frequency is 20 Hz  $\pm$  0.05 Hz.
- 5.5.5 Turn the CALIBRATED DIAL fully counter clockwise and check that the output frequency is 200 Hz  $\pm$  0.5 Hz.
- 5.5.6 Repeat 5.5.2 through 5.5.5 until the dial calibration is within 0.25 per cent at 20 Hz and 200 Hz.
- 5.6 EXACT FREQUENCY CALIBRATION PROCEDURE FOR WIEN BRIDGE OSCILLATOR ON 200 Hz to 2 KHz RANGE
- 5.6.1 Check that steps 5.5.1 through 5.5.3 have been performed on the 20 Hz to 200 Hz range before proceeding to the calibration of the 200 Hz to 2 KHz range since potentiometers A3A1R2 and A3A1R5 are used to set the frequency ratio on both of these ranges.
- 5.6.2 Select the 200 Hz to 2 KHz range and turn the CALIBRATED DIAL fully clockwise. Select A3C9 and A3C17 so that the output frequency is 200 Hz  $\pm$  0.5 Hz.
- 5.6.3 Rotate the CALIBRATED DIAL fully counter clockwise. The oscillator frequency should be very close to 2 KHz. If necessary, reselect A3C9 and A3C17 so as to get the best compromise performance (within the  $\pm$  1.0 per cent frequency accuracy specification) on both ends of the 200 Hz to 2 KHz frequency range.
- 5.7 EXACT FREQUENCY CALIBRATION PROCEDURE FOR WIEN BRIDGE OSCILLATOR ON 2 KHz to 20 KHz RANGE
- 5.7.1 Check that Section 5.4 of this calibration procedure has been performed before proceeding to this section of the calibration procedure.



- 5.7.2 Select the 2 KHz to 20 KHz range and turn the CALIBRATED DIAL fully clockwise. Record the output frequency of the oscillator. Turn the CALIBRATED DIAL fully counter clockwise and record the output frequency. If the ratio of these two frequencies is less than 1.0, rotate A3A1R1 and A3A1R4 clockwise, adjusting both potentiometer an equal amount in the same direction. If the ratio of these two frequencies is greater than 1.0, rotate A3A1R1 and A3A1R4 counter clockwise, adjusting both potentiometers an equal amount in the same direction. If the oscillator stops oscillating during this adjustment procedure, vary A3A1R1 or A3A1R4 somewhat by itself until the oscillation builds up again.
- 5.7.3 Repeat step 5.7.2 until the frequency ratio is exactly 10 to 1.0  $\pm$  0.1 per cent.
- 5.7.4 Turn the CALIBRATED DIAL fully clockwise and select equal value capacitors for A3C7 and A3C15 so that the output frequency is 2 KHz  $\pm$  5 Hz.
- 5.7.5 Turn the CALIBRATED DIAL fully counter clockwise and check that the output frequency is 20 KHz  $\pm$  50 Hz.
- 5.7.6 Repeat step 5.7.4 through 5.7.5 until the dial calibration is within 0.25 per cent at both ends of the CALIBRATED DIAL.
- 5.8 FREQUENCY RESPONSE ADJUSTMENTS
- 5.8.1 The dial calibration procedure outlined in Section 5.4 through 5.7 of this instruction manual should be performed before proceeding to this section.
- 5.8.2 Monitor the amplitude of the sine wave output of the oscillator at the Phase "A" output with the differential voltmeter. Connect the low side of the differential voltmeter to the circuit ground.
- 5.8.3 Select the 2 KHz to 20 KHz range and set the CALIBRATED DIAL to 20 KHz. Record the output voltage. Vary capacitors A3C7 and A3C15 equal amounts but in opposite directions so that the output voltage at 2 KHz (on the 2 KHz to 20 KHz range) is within 50 millivolts of the output voltage previously measured at 20 KHz. Check that this adjustment has not affected the dial calibration to any significant degree on the 2 KHz to 20 KHz range. Reselect capacitors A3C7 and A3C15 if the frequency tolerance exceeds  $\pm$  1 per cent.
- 5.8.4 Select the 200 Hz to 2 KHz range and set the CALIBRATED DIAL to 200 Hz. Select capacitors A3C9 and A3C17 so that the output at 200 Hz is within 50 millivolts of that at 2 KHz in step 5.8.3 of this procedure.



- 5.8.5 Vary the frequency from 200 Hz to 2 KHz and check that the output varies less than 10 millivolts over the 200 Hz to 2 KHz range. If problems are encountered, vary potentiometers A3A1R2 and A3A1R5 equal amounts but in opposite directions until the frequency response on the 200 Hz to 2 KHz range is flat within 10 millivolts.
- 5.8.6 Check that the adjustments in steps 5.8.4 and 5.8.5 have not affected the dial calibration on the 200 Hz to 2 KHz range to any significant degree. Reselect A3C9, A3C17, A3A1R2 and A3A1R5 if the frequency tolerance exceeds  $\pm 1$  per cent.
- 5.8.7 Select the 20 Hz to 200 Hz range and set the CALIBRATED DIAL to 200 Hz. Check that the output voltage is within 10 millivolts of that measured at 200 Hz on the 200 Hz to 2 KHz range as determined in step 5.8.5 of this procedure. If problems are encountered, vary capacitors A3C14 and A3C22 equal amounts but in opposite directions until the output at 200 Hz on the 20 Hz to 200 Hz range is within 10 millivolts of the output at 200 Hz on the 200 Hz to 2 KHz range.
- 5.8.8 Vary the frequency from 20 Hz to 200 Hz and check that the output varies less than 15 millivolts. No adjustment is provided. Repeat steps 5.8.5 through 5.8.7 in such a manner as to obtain the best compromise frequency response on the two lowest frequency ranges, if the output variation exceeds 15 millivolts on the 20 Hz to 20 KHz range.
- 5.8.9 Vary the frequency from 20 Hz to 20 KHz and check that the phase "A" output varies less than  $\pm 100$  millivolts from its value at 400 Hz. Repeat steps 5.8.1 through 5.8.8 if the output voltage variation exceeds this limit.
- 5.9 ABSOLUTE AC OUTPUT VOLTAGE and HARMONIC DISTORTION
- 5.9.1 The frequency response adjustment procedure given in Section 5.8 should be performed before proceeding to this section.
- 5.9.2 Monitor the phase "A" output of the oscillator with the differential voltmeter and a distortion analyzer.
- 5.9.3 Select the 200 Hz to 2 KHz range and set the oscillator frequency to 400 Hz. Select resistor A1R1 so that the AC output voltage is 5.0 volts rms  $\pm 5$  millivolts.
- 5.9.4 Vary the output frequency from 20 Hz to 20 KHz and check that the output voltage remains within a  $\pm 2.0$  per cent band ( $\pm 100$  millivolts) of its value at 400 Hz.



5.9.5 Adjust potentiometer AIRII for approximately 0.08 per cent (but less than 0.1 per cent) harmonic distortion at 45 Hz. If this control is set for excessively low harmonic distortion then the oscillator may not start properly under all conditions.

## 5.10 PHASE ADJUSTMENTS FOR MODEL 800T-20/20K-1-2 $\phi$ TWO PHASE OSCILLATOR

5.10.1 Connect the Model 800T-20/20K-1-2 $\phi$  Oscillator as shown in Figure 5 using either the  $\pm 25$  volt power supply in the California Instruments Invertron<sup>®</sup> or a pair of external  $\pm 25$  volt and -25 volt DC power supplies. Check that the procedure given in Section 5.3 through 5.9 of this instruction manual have been performed before proceeding further.

5.10.2 Rotate the front panel AMPLITUDE control fully clockwise. Connect a differential phase meter from the junction of AIR19 and A1CR7 to the junction of A2R3 and A2R3. The phase shift must be  $-26.45$  degrees  $\pm 0.25$  degrees from 20 Hz to 20 KHz. If problems are encountered, check that steps 5.5 through 5.8 of this procedure have been followed properly. Parameter unbalance in the basic Wien bridge feedback circuit can cause errors in this phase shift.

5.10.3 Set the frequency of the oscillator to 400 Hz and adjust potentiometer A2R2 so that the amplitude of the AC voltage at the junction of A2R3 and A2R17 is equal to the voltage measured at the junction of AIR19 and A1CR7. Vary the oscillator frequency from 20 Hz to 20 KHz. The AC voltage at the junction of A2R3 and A2R17 should track the AC voltage at the junction of AIR19 and A1CR7 within 15 millivolts.

5.10.5 Monitor the phase "A" output of the oscillator with the differential voltmeter and record the AC output voltage from 20 Hz to 20 KHz.

5.10.6 Monitor the phase "C" output of the oscillator with the differential voltmeter and record the AC output voltage from 20 Hz to 20 KHz. The amplitude of the phase "A" and phase "C" output voltages should now correspond within 100 millivolts of one another.

5.10.7 Connect the differential phase meter from the phase "A" output to the phase "C" output. Readjust potentiometer A2R2 slightly so that the phase "C" output leads the phase "A" output by 90 degrees  $\pm 2$  degrees and that the amplitude of the phase "C" output tracks the amplitude of the phase "A" output within 75 millivolts from 20 Hz to 20 KHz. Capacitor A2C9 and/or A2C11 (10 pf or less) have been selected at the factory for the best compromise phase shift and amplitude tracking in the 10 KHz to 20 KHz region.





5.11 PHASE ADJUSTMENTS FOR MODEL 800T-20/20K-1-3ØD THREE PHASE, OPEN DELTA OSCILLATOR

5.11.1 Connect the 800T-20/20K-1-3ØD oscillator as shown in Figure 5 and repeat step 5.10.1 through 5.10.7 except check that the phase shift is 60 degrees  $\pm$  2 degrees.

5.12 PHASE ADJUSTMENTS FOR MODEL 800T-20/20K-1-3Ø THREE PHASE OSCILLATOR

5.12.1 Connect the 800T-20/20K-1-3Ø oscillator as shown in Figure 5 and repeat steps 5.10.1 through 5.10.7 except check that the phase shift is 120 degrees  $\pm$  2 degrees.

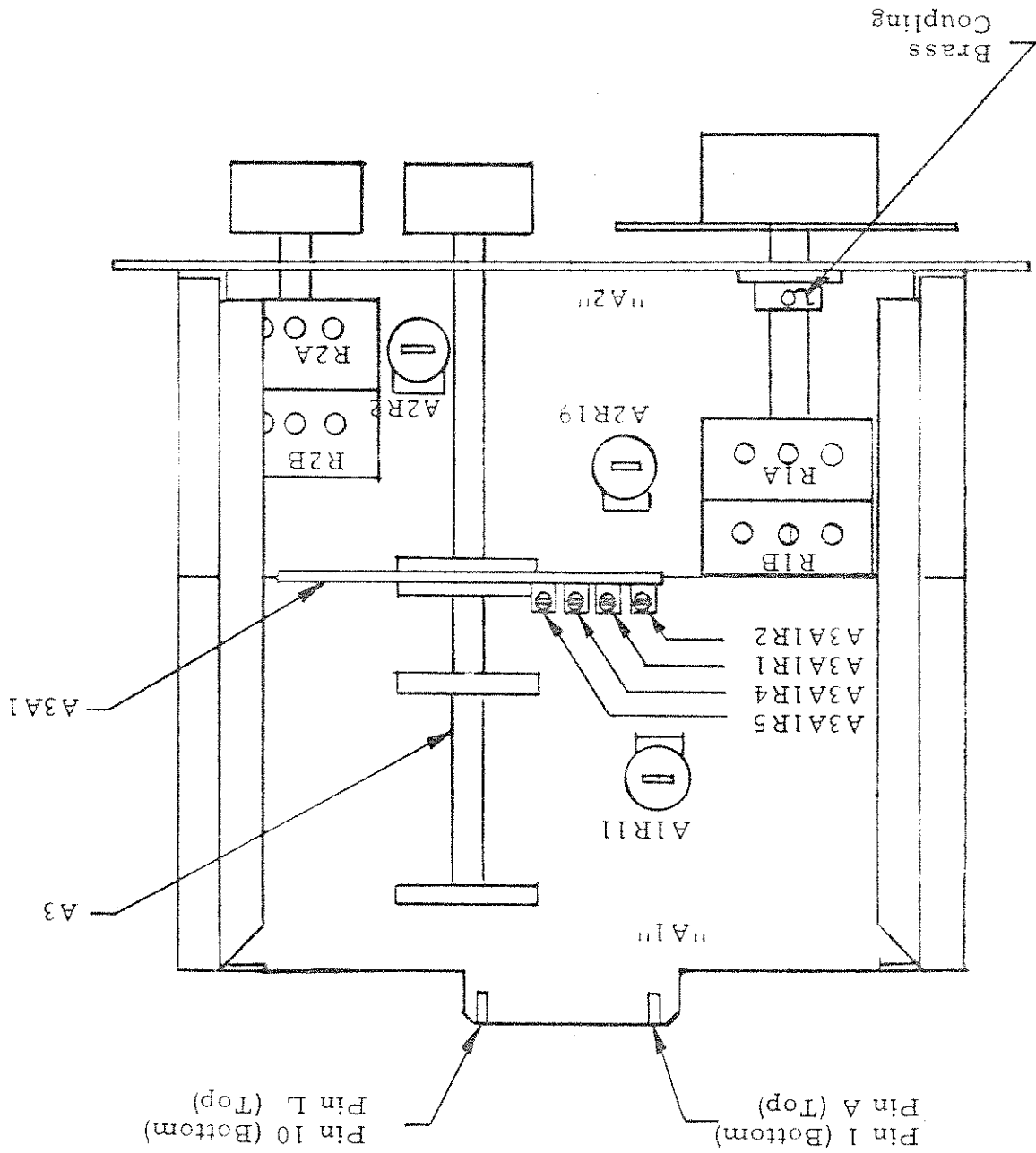
5.12.2 Set the frequency of the oscillator to 400 Hz and adjust AZR19 so that the phase "B" output amplitude is equal to the phase "A" output amplitude.

5.12.3 Monitor the phase "A" and the phase "B" output of the three phase oscillator with the differential voltmeter. Select capacitor AZC10 (20 pf or less) so that the frequency response of the phase "B" output most closely tracks that of the phase "A" output in the region from 10 KHz to 20 KHz. Check that the phase "A" output and the phase "B" output are equal to one another within 100 millivolts rms from 20 Hz to 20 KHz.

5.12.4 Vary the frequency of the oscillator from 20 Hz to 20 KHz and check that the phase shift from phase "A" to phase "B" remains within 120  $\pm$  2 degrees.



FIGURE 6. Locations of Internal Adjustment Potentiometers.





## MAINTENANCE AND TROUBLESHOOTING

### 6.1 GENERAL

The California Instruments 800T-20/20K Series Variable Frequency Oscillators are completely solid state and should provide years of trouble-free service. Since the instrument contains no moving parts, periodic maintenance is limited to cleaning in accordance with good commercial shop practices, and a periodic check of frequency accuracy and distortion at six month intervals. If the oscillator is operated in heavy duty applications where severe temperature extremes or mechanical shock are encountered, it is recommended that a more frequent calibration schedule be established.

### 6.2 TROUBLESHOOTING

6.2.1 Before attempting to repair the oscillator, check that the controls are properly set, that the DC input power is correct and that an excessive load is not connected to the output of the oscillator. Any load impedance less than 5 K ohms per phase should be considered excessive.

6.2.2 Check that the  $\pm 5$  volt regulator circuits are operating properly and that they have less than 20 millivolts peak-to-peak ripple.

6.2.3 Check that the basic Wien bridge oscillator is operating satisfactorily. Detailed information on this circuit is given in Section 3.1 and Section 5.1 through 5.9 of this instruction manual.

6.2.4 Check that the servo feedback loop is operating satisfactorily. Detailed information on this circuitry is given in Section 3.1 through Section 3.1.6 of this manual. If the problem appears to be in this area, disconnect resistor AIR8 at AIIC3 Pin 6 and apply a +2 volt to +10 volt DC bias to AIR8. This opens the servo feedback loop and allows servicing of this circuitry by conventional signal tracing methods.

6.2.5 If the problem is in the single phase to multi-phase converter, the circuit may be checked by conventional signal tracing methods. Detailed information on the single phase to multi-phase converter circuit is given in Section 3.2 of this instruction manual.



## CIRCUIT DIAGRAMS

### 7.1 GENERAL

This section contains schematics and mechanical diagrams necessary for operation and maintenance of the 800T Series Variable Frequency Oscillators. The schematic diagrams illustrate the circuit while the mechanical assemblies indicate the part placement.

### 7.2 REFERENCE DESIGNATIONS

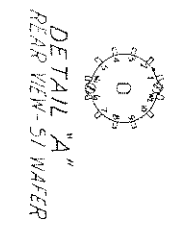
Partial reference designators are shown on schematics and mechanical drawings. Prefix these reference designators with assembly and/or sub-assembly designation for the complete reference designator. For example:

Assembly/Sub-Assembly	Component	Complete Designation
A1	C30	A1C30
None	T1	T1
A3	CRI	A3CRI
A3A1	R1	A3A1R1





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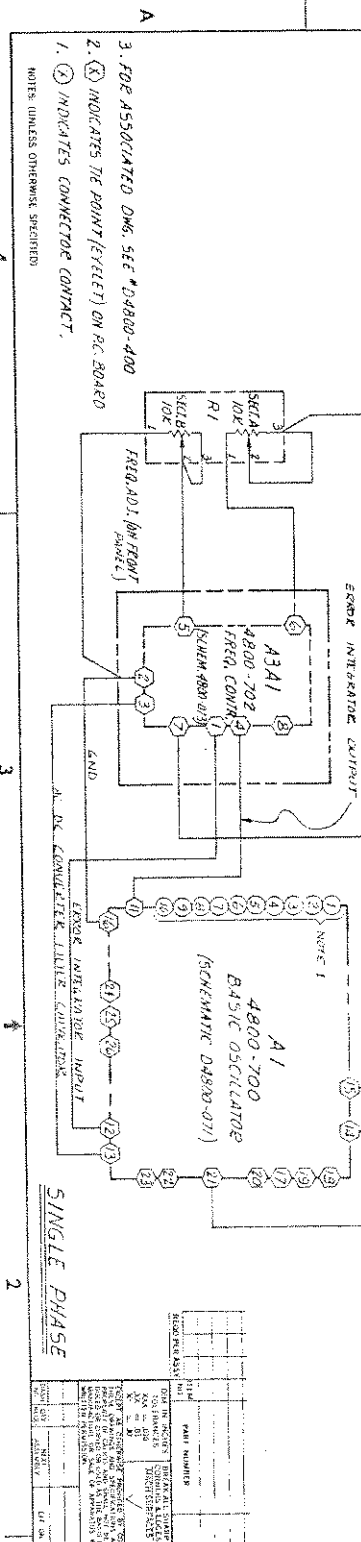
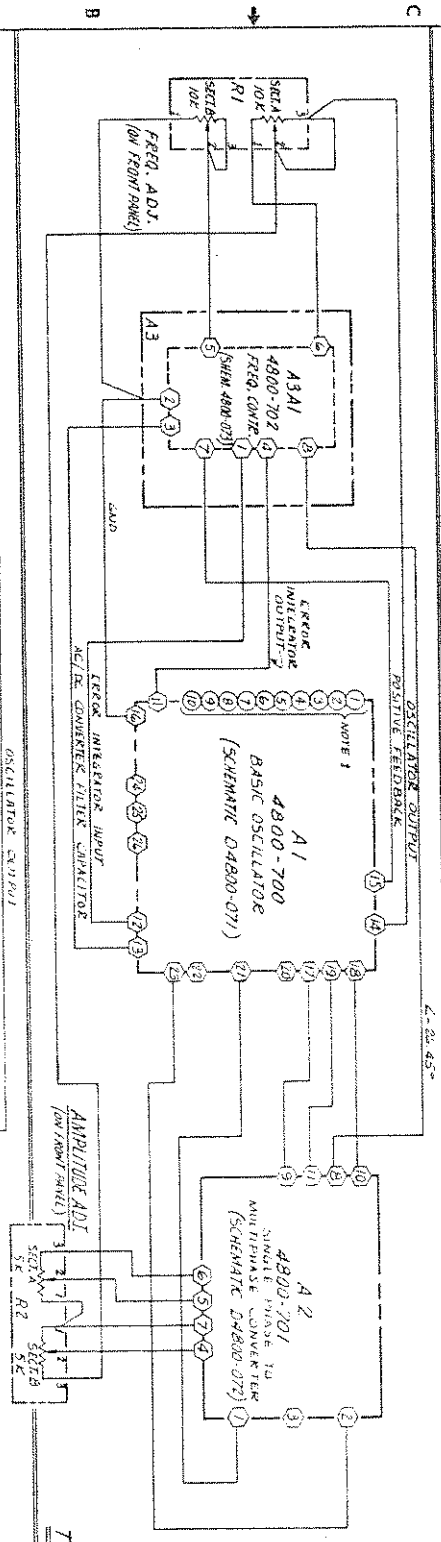
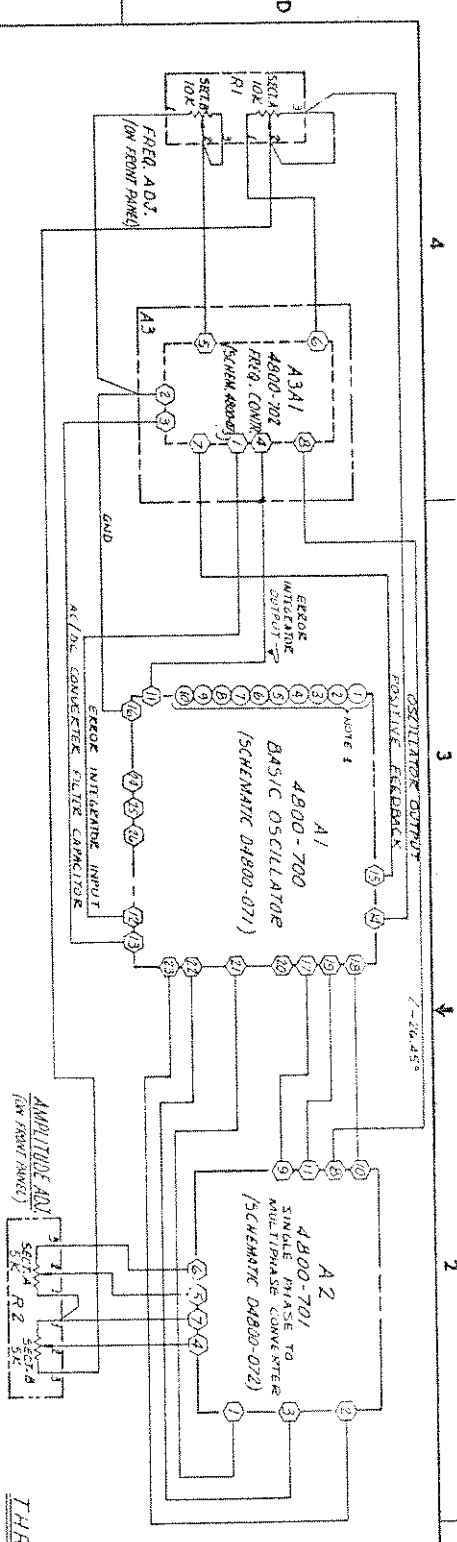


**WIRING TABLE 1**

WIRE FROM	WIRE TO	WIRE REMARKS
1	A1-21	R1 SEC A-2 24 AWG BRN
2	A1-12	R3 SEC 4 BRN
3	A1-15	R3 SEC 3 BRN
4	A1-13	R3 SEC 2 BRN
5	A1-14	R3 SEC 1 BRN
6	A1-16	R3 SEC 5 BRN
7	A1-17	R3 SEC 6 BRN
8	A1-18	R3 SEC 7 BRN
9	A1-19	R3 SEC 8 BRN
10	A1-20	R3 SEC 9 BRN
11	A1-22	R3 SEC 10 BRN
12	A1-23	R3 SEC 11 BRN
13	A1-24	R3 SEC 12 BRN
14	A1-25	R3 SEC 13 BRN
15	A1-26	R3 SEC 14 BRN
16	A1-27	R3 SEC 15 BRN
17	A1-28	R3 SEC 16 BRN
18	A1-29	R3 SEC 17 BRN
19	A1-30	R3 SEC 18 BRN
20	A1-31	R3 SEC 19 BRN
21	A1-32	R3 SEC 20 BRN
22	A1-33	R3 SEC 21 BRN
23	A1-34	R3 SEC 22 BRN
24	A1-35	R3 SEC 23 BRN
25	A1-36	R3 SEC 24 BRN
26	A1-37	R3 SEC 25 BRN
27	A1-38	R3 SEC 26 BRN
28	A1-39	R3 SEC 27 BRN
29	A1-40	R3 SEC 28 BRN
30	A1-41	R3 SEC 29 BRN
31	A1-42	R3 SEC 30 BRN
32	A1-43	R3 SEC 31 BRN
33	A1-44	R3 SEC 32 BRN
34	A1-45	R3 SEC 33 BRN
35	A1-46	R3 SEC 34 BRN
36	A1-47	R3 SEC 35 BRN
37	A1-48	R3 SEC 36 BRN
38	A1-49	R3 SEC 37 BRN
39	A1-50	R3 SEC 38 BRN
40	A1-51	R3 SEC 39 BRN
41	A1-52	R3 SEC 40 BRN
42	A1-53	R3 SEC 41 BRN
43	A1-54	R3 SEC 42 BRN
44	A1-55	R3 SEC 43 BRN
45	A1-56	R3 SEC 44 BRN
46	A1-57	R3 SEC 45 BRN
47	A1-58	R3 SEC 46 BRN
48	A1-59	R3 SEC 47 BRN
49	A1-60	R3 SEC 48 BRN
50	A1-61	R3 SEC 49 BRN
51	A1-62	R3 SEC 50 BRN
52	A1-63	R3 SEC 51 BRN
53	A1-64	R3 SEC 52 BRN
54	A1-65	R3 SEC 53 BRN
55	A1-66	R3 SEC 54 BRN
56	A1-67	R3 SEC 55 BRN
57	A1-68	R3 SEC 56 BRN
58	A1-69	R3 SEC 57 BRN
59	A1-70	R3 SEC 58 BRN
60	A1-71	R3 SEC 59 BRN
61	A1-72	R3 SEC 60 BRN
62	A1-73	R3 SEC 61 BRN
63	A1-74	R3 SEC 62 BRN
64	A1-75	R3 SEC 63 BRN
65	A1-76	R3 SEC 64 BRN
66	A1-77	R3 SEC 65 BRN
67	A1-78	R3 SEC 66 BRN
68	A1-79	R3 SEC 67 BRN
69	A1-80	R3 SEC 68 BRN
70	A1-81	R3 SEC 69 BRN
71	A1-82	R3 SEC 70 BRN
72	A1-83	R3 SEC 71 BRN
73	A1-84	R3 SEC 72 BRN
74	A1-85	R3 SEC 73 BRN
75	A1-86	R3 SEC 74 BRN
76	A1-87	R3 SEC 75 BRN
77	A1-88	R3 SEC 76 BRN
78	A1-89	R3 SEC 77 BRN
79	A1-90	R3 SEC 78 BRN
80	A1-91	R3 SEC 79 BRN
81	A1-92	R3 SEC 80 BRN
82	A1-93	R3 SEC 81 BRN
83	A1-94	R3 SEC 82 BRN
84	A1-95	R3 SEC 83 BRN
85	A1-96	R3 SEC 84 BRN
86	A1-97	R3 SEC 85 BRN
87	A1-98	R3 SEC 86 BRN
88	A1-99	R3 SEC 87 BRN
89	A1-100	R3 SEC 88 BRN
90	A1-101	R3 SEC 89 BRN
91	A1-102	R3 SEC 90 BRN
92	A1-103	R3 SEC 91 BRN
93	A1-104	R3 SEC 92 BRN
94	A1-105	R3 SEC 93 BRN
95	A1-106	R3 SEC 94 BRN
96	A1-107	R3 SEC 95 BRN
97	A1-108	R3 SEC 96 BRN
98	A1-109	R3 SEC 97 BRN
99	A1-110	R3 SEC 98 BRN
100	A1-111	R3 SEC 99 BRN
101	A1-112	R3 SEC 100 BRN
102	A1-113	R3 SEC 101 BRN
103	A1-114	R3 SEC 102 BRN
104	A1-115	R3 SEC 103 BRN
105	A1-116	R3 SEC 104 BRN
106	A1-117	R3 SEC 105 BRN
107	A1-118	R3 SEC 106 BRN
108	A1-119	R3 SEC 107 BRN
109	A1-120	R3 SEC 108 BRN
110	A1-121	R3 SEC 109 BRN
111	A1-122	R3 SEC 110 BRN
112	A1-123	R3 SEC 111 BRN
113	A1-124	R3 SEC 112 BRN
114	A1-125	R3 SEC 113 BRN
115	A1-126	R3 SEC 114 BRN
116	A1-127	R3 SEC 115 BRN
117	A1-128	R3 SEC 116 BRN
118	A1-129	R3 SEC 117 BRN
119	A1-130	R3 SEC 118 BRN
120	A1-131	R3 SEC 119 BRN
121	A1-132	R3 SEC 120 BRN
122	A1-133	R3 SEC 121 BRN
123	A1-134	R3 SEC 122 BRN
124	A1-135	R3 SEC 123 BRN
125	A1-136	R3 SEC 124 BRN
126	A1-137	R3 SEC 125 BRN
127	A1-138	R3 SEC 126 BRN
128	A1-139	R3 SEC 127 BRN
129	A1-140	R3 SEC 128 BRN
130	A1-141	R3 SEC 129 BRN
131	A1-142	R3 SEC 130 BRN
132	A1-143	R3 SEC 131 BRN
133	A1-144	R3 SEC 132 BRN
134	A1-145	R3 SEC 133 BRN
135	A1-146	R3 SEC 134 BRN
136	A1-147	R3 SEC 135 BRN
137	A1-148	R3 SEC 136 BRN
138	A1-149	R3 SEC 137 BRN
139	A1-150	R3 SEC 138 BRN
140	A1-151	R3 SEC 139 BRN
141	A1-152	R3 SEC 140 BRN
142	A1-153	R3 SEC 141 BRN
143	A1-154	R3 SEC 142 BRN
144	A1-155	R3 SEC 143 BRN
145	A1-156	R3 SEC 144 BRN
146	A1-157	R3 SEC 145 BRN
147	A1-158	R3 SEC 146 BRN
148	A1-159	R3 SEC 147 BRN
149	A1-160	R3 SEC 148 BRN
150	A1-161	R3 SEC 149 BRN
151	A1-162	R3 SEC 150 BRN
152	A1-163	R3 SEC 151 BRN
153	A1-164	R3 SEC 152 BRN
154	A1-165	R3 SEC 153 BRN
155	A1-166	R3 SEC 154 BRN
156	A1-167	R3 SEC 155 BRN
157	A1-168	R3 SEC 156 BRN
158	A1-169	R3 SEC 157 BRN
159	A1-170	R3 SEC 158 BRN
160	A1-171	R3 SEC 159 BRN
161	A1-172	R3 SEC 160 BRN
162	A1-173	R3 SEC 161 BRN
163	A1-174	R3 SEC 162 BRN
164	A1-175	R3 SEC 163 BRN
165	A1-176	R3 SEC 164 BRN
166	A1-177	R3 SEC 165 BRN
167	A1-178	R3 SEC 166 BRN
168	A1-179	R3 SEC 167 BRN
169	A1-180	R3 SEC 168 BRN
170	A1-181	R3 SEC 169 BRN
171	A1-182	R3 SEC 170 BRN
172	A1-183	R3 SEC 171 BRN
173	A1-184	R3 SEC 172 BRN
174	A1-185	R3 SEC 173 BRN
175	A1-186	R3 SEC 174 BRN
176	A1-187	R3 SEC 175 BRN
177	A1-188	R3 SEC 176 BRN
178	A1-189	R3 SEC 177 BRN
179	A1-190	R3 SEC 178 BRN
180	A1-191	R3 SEC 179 BRN
181	A1-192	R3 SEC 180 BRN
182	A1-193	R3 SEC 181 BRN
183	A1-194	R3 SEC 182 BRN
184	A1-195	R3 SEC 183 BRN
185	A1-196	R3 SEC 184 BRN
186	A1-197	R3 SEC 185 BRN
187	A1-198	R3 SEC 186 BRN
188	A1-199	R3 SEC 187 BRN
189	A1-200	R3 SEC 188 BRN
190	A1-201	R3 SEC 189 BRN
191	A1-202	R3 SEC 190 BRN
192	A1-203	R3 SEC 191 BRN
193	A1-204	R3 SEC 192 BRN
194	A1-205	R3 SEC 193 BRN
195	A1-206	R3 SEC 194 BRN
196	A1-207	R3 SEC 195 BRN
197	A1-208	R3 SEC 196 BRN
198	A1-209	R3 SEC 197 BRN
199	A1-210	R3 SEC 198 BRN
200	A1-211	R3 SEC 199 BRN
201	A1-212	R3 SEC 200 BRN
202	A1-213	R3 SEC 201 BRN
203	A1-214	R3 SEC 202 BRN
204	A1-215	R3 SEC 203 BRN
205	A1-216	R3 SEC 204 BRN
206	A1-217	R3 SEC 205 BRN
207	A1-218	R3 SEC 206 BRN
208	A1-219	R3 SEC 207 BRN
209	A1-220	R3 SEC 208 BRN
210	A1-221	R3 SEC 209 BRN
211	A1-222	R3 SEC 210 BRN
212	A1-223	R3 SEC 211 BRN
213	A1-224	R3 SEC 212 BRN
214	A1-225	R3 SEC 213 BRN
215	A1-226	R3 SEC 214 BRN
216	A1-227	R3 SEC 215 BRN
217	A1-228	R3 SEC 216 BRN
218	A1-229	R3 SEC 217 BRN
219	A1-230	R3 SEC 218 BRN
220	A1-231	R3 SEC 219 BRN
221	A1-232	R3 SEC 220 BRN
222	A1-233	R3 SEC 221 BRN
223	A1-234	R3 SEC 222 BRN
224	A1-235	R3 SEC 223 BRN
225	A1-236	R3 SEC 224 BRN
226	A1-237	R3 SEC 225 BRN
227	A1-238	R3 SEC 226 BRN
228	A1-239	R3 SEC 227 BRN
229	A1-240	R3 SEC 228 BRN
230	A1-241	R3 SEC 229 BRN
231	A1-242	R3 SEC 230 BRN
232	A1-243	R3 SEC 231 BRN
233	A1-244	R3 SEC 232 BRN
234	A1-245	R3 SEC 233 BRN
235	A1-246	R3 SEC 234 BRN
236	A1-247	R3 SEC 235 BRN
237	A1-248	R3 SEC 236 BRN
238	A1-249	R3 SEC 237 BRN
239	A1-250	R3 SEC 238 BRN
240	A1-251	R3 SEC 239 BRN
241	A1-252	R3 SEC 240 BRN
242	A1-253	R3 SEC 241 BRN
243	A1-254	R3 SEC 242 BRN
244	A1-255	R3 SEC 243 BRN
245	A1-256	R3 SEC 244 BRN
246	A1-257	R3 SEC 245 BRN
247	A1-258	R3 SEC 246 BRN
248	A1-259	R3 SEC 247 BRN
249	A1-260	R3 SEC 248 BRN
250	A1-261	R3 SEC 249 BRN
251	A1-262	R3 SEC 250 BRN
252	A1-263	R3 SEC 251 BRN
253	A1-264	R3 SEC 252 BRN
254	A1-265	R3 SEC 253 BRN
255	A1-266	R3 SEC 254 BRN
256	A1-267	R3 SEC 255 BRN
257	A1-268	R3 SEC 256 BRN
258	A1-269	R3 SEC 257 BRN
259	A1-270	R3 SEC 258 BRN
260	A1-271	R3 SEC 259 BRN
261	A1-272	R3 SEC 260 BRN
262	A1-273	R3 SEC 261 BRN
263	A1-274	R3 SEC 262 BRN
264	A1-275	R3 SEC 263 BRN
265	A1-276	R3 SEC 264 BRN
266	A1-277	R3 SEC 265 BRN
267	A1-278	R3 SEC 266 BRN
268	A1-279	R3 SEC 267 BRN
269	A1-280	R3 SEC 268 BRN
270	A1-281	R3 SEC 269 BRN
271	A1-282	R3 SEC 270 BRN
272	A1-283	R3 SEC 271 BRN
273	A1-284	R3 SEC 272 BRN
274	A1-285	R3 SEC 273 BRN
275	A1-286	R3 SEC 274 BRN
276	A1-287	R3 SEC 275 BRN
277	A1-288	R3 SEC 276 BRN
278	A1-289	R3 SEC 277 BRN
279	A1-290	R3 SEC 278 BRN
280	A1-291	R3 SEC 279 BRN
281	A1-292	R3 SEC 280 BRN
282	A1-293	R3 SEC 281 BRN
283	A1-294	R3 SEC 282 BRN
284	A1-295	R3 SEC 283 BRN
285	A1-296	R3 SEC 284 BRN
286	A1-297	R3 SEC 285 BRN
287	A1-298	R3 SEC 286 BRN
288	A1-299	R3 SEC 287 BRN
289	A1-300	R3 SEC 288 BRN
290	A1-301	R3 SEC 289 BRN
291	A1-302	R3 SEC 290 BRN
292	A1-303	R3 SEC 291 BRN
293	A1-304	R3 SEC 292 BRN
294	A1-305	R3 SEC 293 BRN
295	A1-306	R3 SEC 294 BRN
296	A1-307	R3 SEC 295 BRN
297	A1-308	R3 SEC 296 BRN
298	A1-309	R3 SEC 297 BRN
299	A1-310	R3 SEC 298 BRN
300	A1-311	R3 SEC 299 BRN
301	A1-312	R3 SEC 300 BRN
302	A1-313	R3 SEC 301 BRN
303	A1-314	R3 SEC 302 BRN
304	A1-315	R3 SEC 303 BRN
305	A1-316	R3 SEC 304 BRN
306	A1-317	R3 SEC 305 BRN
307	A1-318	R3 SEC 306 BRN
308	A1-319	R3 SEC 307 BRN
309	A1-320	R3 SEC 308 BRN
310	A1-321	R3 SEC 309 BRN
311	A1-322	R3 SEC 310 BRN
312	A1-323	R3 SEC 311 BRN
313	A1-324	R3 SEC 312 BRN
314	A1-325	R3 SEC 313 BRN
315	A1-326	R3 SEC 314 BRN
316	A1-327	R3 SEC 315 BRN
317	A1-328	R3 SEC 316 BRN
318	A1-329	R3 SEC 317 BRN
319	A1-330	R3 SEC 318 BRN
320	A1-331	R3 SEC 319 BRN
321	A1-332	R3 SEC 320 BRN
322	A1-333	R3 SEC 321 BRN
323	A1-334	R3 SEC 322 BRN
324	A1-335	R3 SEC 323 BRN
325	A1-336	R3 SEC 324 BRN
326	A1-337	R3 SEC 325 BRN
327	A1-338	R3 SEC 326 BRN
328	A1-339	R3 SEC 327 BRN
329	A1-340	R3 SEC 328 BRN
330	A1-341	R3 SEC 329 BRN
331	A1-342	R3 SEC 330 BRN
332	A1-343	R3 SEC 331 BRN
333	A1-344	R3 SEC 332 BRN
334	A1-345	R3 SEC 333 BRN
335	A1-346	R3 SEC 334 BRN
336	A1-347	R3 SEC 335 BRN
337	A1-348	R3 SEC 336 BRN
338	A1-349	R3 SEC 337 BRN
339	A1-350	R3 SEC 338 BRN
340	A1-351	R3 SEC 339 BRN
341	A1-352	R3 SEC 340 BRN
342	A1-353	R3 SEC 341 BRN
343	A1-354	R3 SEC 34



REV. NO.		DESCRIPTION	DATE	BY	CHKD.
1	A	D.C. REVISIONS	1/18/54		
2	B	REVISIONS	1/18/54		
3	C	INC TO 401	1/18/54		



REV. NO.	DESCRIPTION	DATE	BY	CHKD.
1	D	INNER CONNECTING DIAGRAM	1/18/54	
2				
3				

**CALICO**  
 MODEL 8007 401/20KHz  
 16067  
 D4800-070

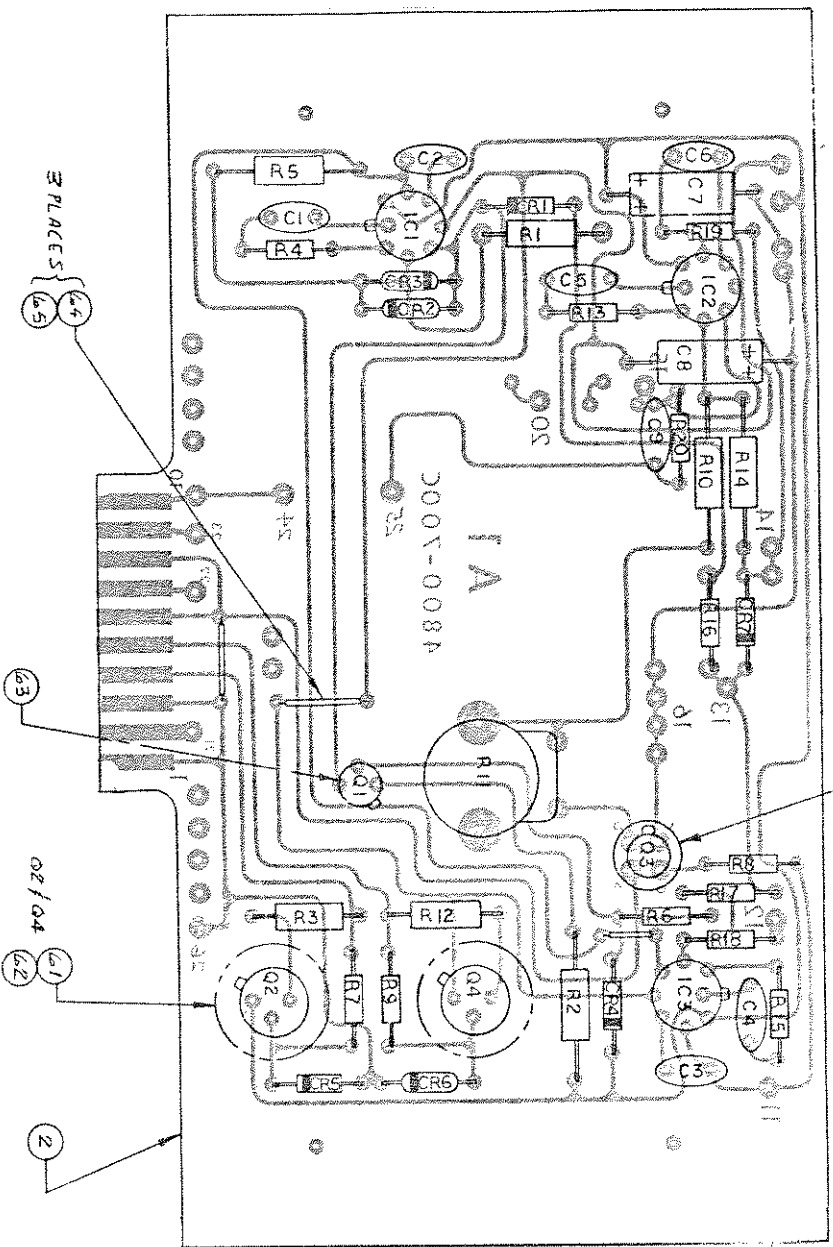
- FOR ASSOCIATED DWG. SEE # D4800-400
- INDICATES THE POINT (LEVEL) ON P.C. BOARD
- INDICATES CONNECTOR CONTACT.

NOTES: (UNLESS OTHERWISE SPECIFIED)

D4800-070



REVISIONS		ZONE	DATE	APPRO
REV	AUTH	DESCRIPTION		



-1 P.C. BOARD ASSY  
VIEW COMPONENT SIDE  
TOP CLAD DELETED

A1

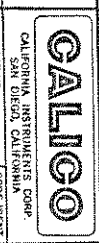
1. FOR SCHEMATIC DIAGRAM, SEE DWG NO. 04800-071.

NOTES: (UNLESS OTHERWISE SPECIFIED)

ITEM	PART NUMBER	PARTS LIST	SHEETS 4 of 5	MATERIAL - SPECIFICATION	CODE IDENT	FINISH	REF DES	ZONE
-1	PL 4800-700	PARTS LIST						

ITEM	DESCRIPTION	QTY	UNIT	ASSEMBLY	DATE	BY	CHKD	APP'D
1	PL 4800-700							

MODEL	DATE	DESIGN	CHKD	APP'D	SCALE	SHEET	OF
BASIC OSCILLATOR P.C. BOARD ASSEMBLY A1	4/1/71	W. J. ...	...	...	2X	2	5



4 3 2 1 A1

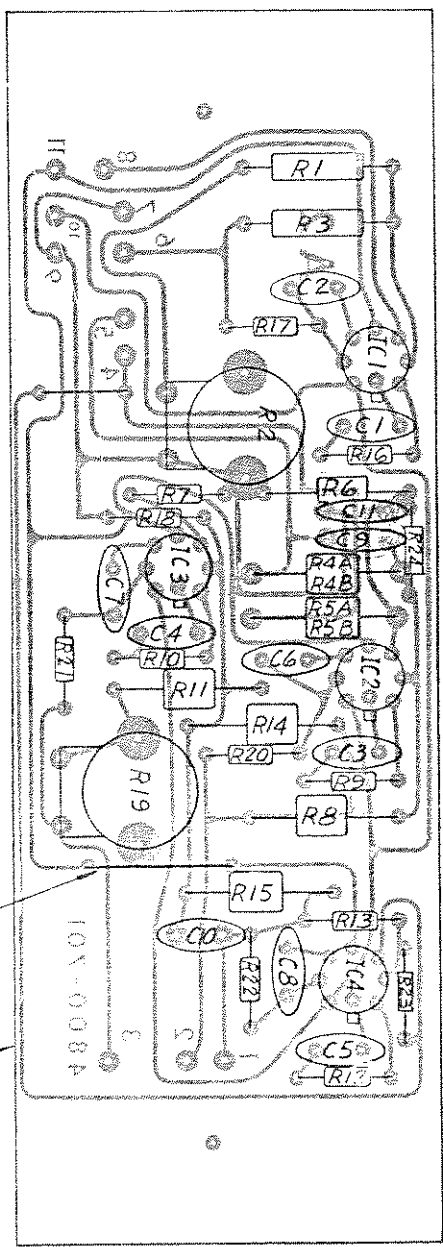








REV		AUTH		DESCRIPTION		ZONING		DATE		APPRO	



- 1 P.C. BOARD ASSY  
 VIEW COMPONENT SIDE  
 A2

1. FOR SCHEMATIC DIAGRAM SEE DWG. NO. D4800-072  
 NOTES: (UNLESS OTHERWISE SPECIFIED)

ITEM NO.	DESCRIPTION	QUANTITY	UNIT	REMARKS
1	P4800-701	1	PCB	

DATE	BY	CHECKED	APPROVED
9/24/74	J. J. [Signature]	[Signature]	[Signature]

DATE	BY	CHECKED	APPROVED
9/24/74	J. J. [Signature]	[Signature]	[Signature]

DATE	BY	CHECKED	APPROVED
9/24/74	J. J. [Signature]	[Signature]	[Signature]

DATE	BY	CHECKED	APPROVED
9/24/74	J. J. [Signature]	[Signature]	[Signature]

DATE	BY	CHECKED	APPROVED
9/24/74	J. J. [Signature]	[Signature]	[Signature]

DATE	BY	CHECKED	APPROVED
9/24/74	J. J. [Signature]	[Signature]	[Signature]

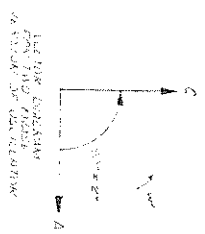
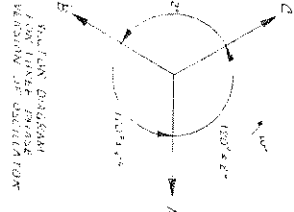
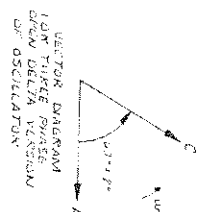
DATE	BY	CHECKED	APPROVED
9/24/74	J. J. [Signature]	[Signature]	[Signature]

DATE	BY	CHECKED	APPROVED
9/24/74	J. J. [Signature]	[Signature]	[Signature]



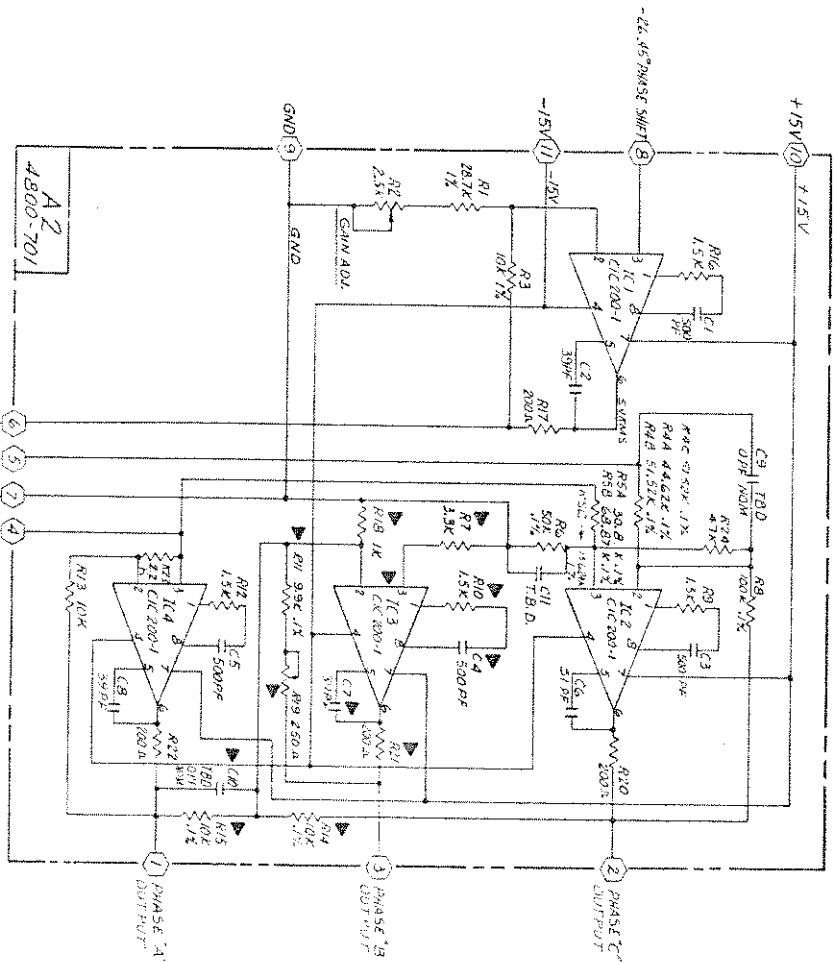
REV. NO.		REVISIONS		DATE	BY	CHKD.
1	1	REVISED	10/15/71			
2	2	REVISED	10/15/71			
3	3	REVISED	10/15/71			
4	4	REVISED	10/15/71			
5	5	REVISED	10/15/71			



NO.	REV.	DESCRIPTION	DATE	BY	CHKD.
1	1	REVISED	10/15/71		
2	2	REVISED	10/15/71		
3	3	REVISED	10/15/71		
4	4	REVISED	10/15/71		
5	5	REVISED	10/15/71		

DESIGNER	DATE	BY	CHKD.
10057	10/15/71		

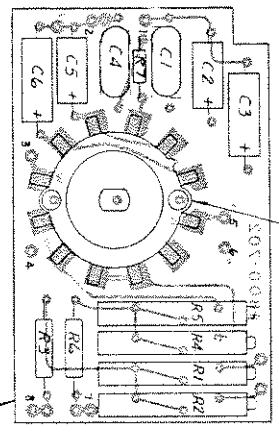


- FOR INNER CONNECTING DIAGRAM SEE DWG. #04800-0701 (REV. 10/15/71)
- AND #04800-074 (REV. 10/15/71) FOR 45/75HZ (300/300HZ)
- INDICATES THE MOUNTING POINT ON P.C. BOARD
- ALL REF. DES. ENDING IN 'A' ARE FOR 50% OPEN DELTA OPERATION.
- ALL REF. DES. ENDING IN 'B' ARE FOR 2/3 OPERATION.
- ALL REF. DES. ENDING IN 'C' ARE FOR 2/3 OPERATION.
- NOT USED FOR TWO PHASE OSCILLATOR ON PWB 'A'
- OPEN DATA OSCILLATOR
- FOR P.C. BOARD ASSY SEE DWG. #4800-701
- RESISTORS ARE 1/4 W 5%

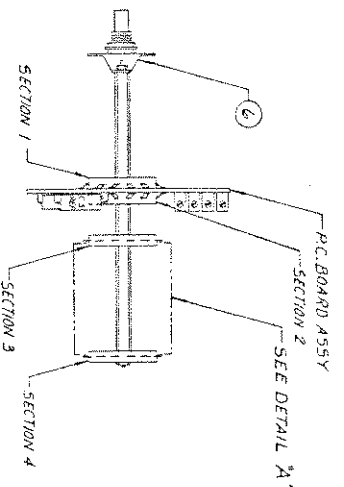
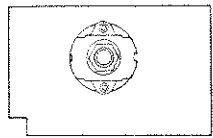
NOTES: UNLESS OTHERWISE SPECIFIED



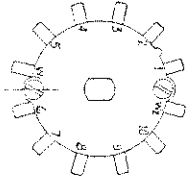
REVISIONS	
REV	DESCRIPTION
1	ISSUED
2	REVISED
3	REVISED
4	REVISED



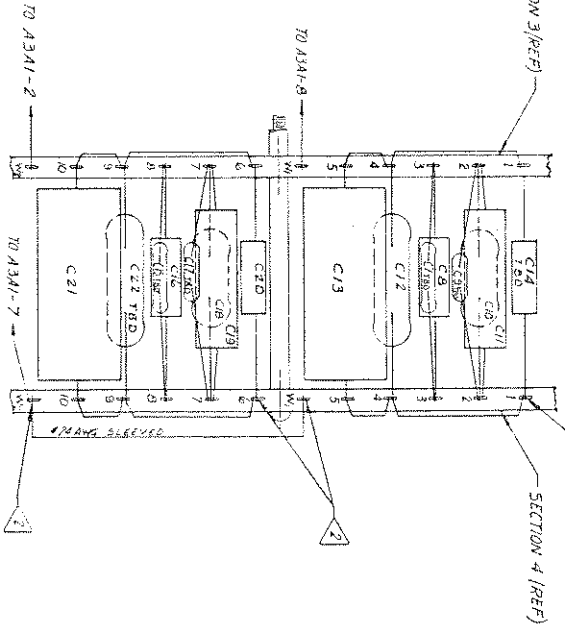
P.C. BOARD ASSY  
VIEW COMPONENT SIDE



- I SWITCH ASSY  
SCALE 1:1



REAR VIEW



SIDE VIEW  
SECTIONS 3 & 4

DETAIL 'A'

1. FOR SCHEMATIC SEE DWG. NO. D4800-073

NOTES: DIMENSIONS OTHERWISE SPECIFIED

REV	DATE	BY	CHKD	DESCRIPTION
1				ISSUED
2				REVISED
3				REVISED
4				REVISED

PROJECT NO.	D4800-702	DATE	APR 19 1967
REV.	1	BY	WJ
DESIGNER	WJ	CHECKED	WJ
ENGINEER	WJ	APPROVED	WJ
MANUFACTURER	WJ	DATE	APR 19 1967
TESTER	WJ	DATE	APR 19 1967
INSPECTOR	WJ	DATE	APR 19 1967

ITEM	QTY	UNIT	DESCRIPTION
1	1	PCB	PC BOARD ASSY
2	1	SW	SWITCH ASSY
3	1	WAFER	WAFER SOLDER

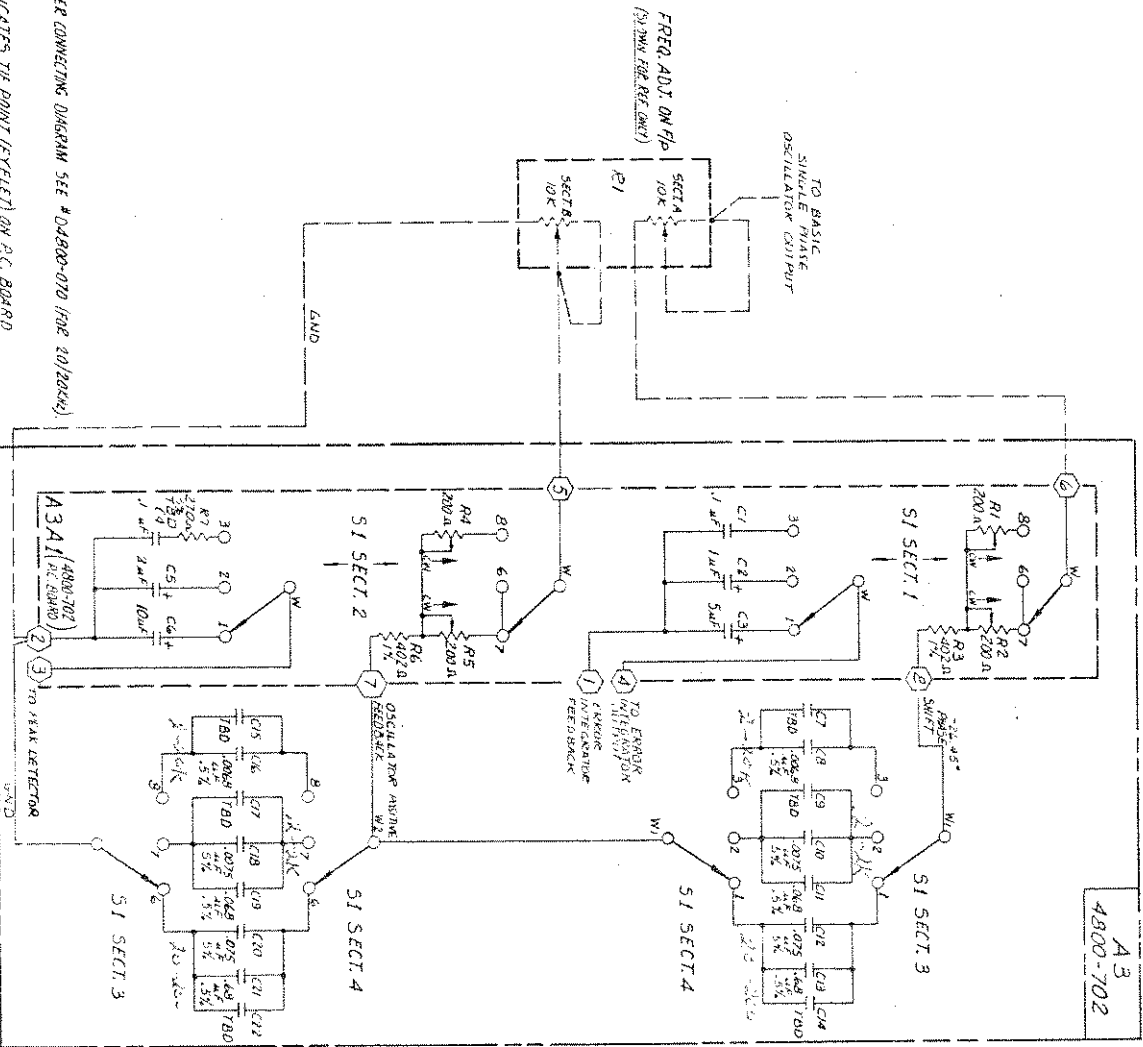
  

PROJECT NO.	D4800-702	SHEET NO.	2 OF 5
REV.	1	DATE	APR 19 1967
DESIGNER	WJ	CHECKED	WJ
ENGINEER	WJ	APPROVED	WJ
MANUFACTURER	WJ	DATE	APR 19 1967
TESTER	WJ	DATE	APR 19 1967
INSPECTOR	WJ	DATE	APR 19 1967

D4800-702



4 3 2



A 3  
4800-702

REVISIONS		DATE	BY
1	INC. C.D. 3012	11/15/57	WJH
2	INC. EO 4014 390	11/15/57	WJH

1. FOR SWITCH ASSY SEE DWG #4800-702.
2. (X) INDICATES THE POINT (LEVEL) ON P.C. BOARD.
3. FOR INNER CONNECTING DIAGRAM SEE #D4800-070 (FOR 20/20KHZ).

NOTES (UNLESS OTHERWISE SPECIFIED)

PROJECT NO.	D4800-073 B
REV. NO.	1
DATE	11/15/57
BY	WJH
CHECKED	WJH
APPROVED	WJH
DESIGNED	WJH
DRAWN	WJH
TESTED	WJH
ASSEMBLED	WJH
OPERATED	WJH
MAINTENANCE	WJH
REWORK	WJH
REVISIONS	WJH
DESCRIPTION	SCHEMATIC DIAGRAM FRFQ. CONTR. NETWORK
DATE	11/15/57
BY	WJH
CHECKED	WJH
APPROVED	WJH
DESIGNED	WJH
DRAWN	WJH
TESTED	WJH
ASSEMBLED	WJH
OPERATED	WJH
MAINTENANCE	WJH
REWORK	WJH
REVISIONS	WJH
DATE	11/15/57
BY	WJH
CHECKED	WJH
APPROVED	WJH
DESIGNED	WJH
DRAWN	WJH
TESTED	WJH
ASSEMBLED	WJH
OPERATED	WJH
MAINTENANCE	WJH
REWORK	WJH
REVISIONS	WJH

D4800-073 B





## REPLACEABLE PARTS

### 8.1 GENERAL

This section contains ordering information and complete list of replaceable parts. Parts are listed by major assembly in alphabetical order of their reference designators. Description, manufacturers' part number, manufacturers' code ident numbers (see Appendix A for list of manufacturers), and California Instruments' stock numbers are indicated.

### 8.2 ORDERING INFORMATION

In order to provide our customers with prompt service on replacement parts, please provide the following information, when applicable, for each part ordered:

- a) Model number and serial number of the instrument.
- b) California Instruments part number of the sub-assembly where component is located.
- c) Component reference designator.
- d) Component description.
- e) Component manufacturer number and code ident.
- f) California Instruments stock number.

All replacement parts orders should be placed with CALIFORNIA INSTRUMENTS COMPANY, a division of Aiken Industries, San Diego, California, 92111.





CALIFORNIA INSTRUMENTS CORP.  
SAN DIEGO, CALIFORNIA

CODE IDENT  
16067

PARTS LIST

REF. DES.	MFG. NUMBER	DESCRIPTION	CODE IDENT	STOCK NUMBER	ITEM NO.
	4800-400-0	Osc. Common Denominator	16067		1
	4800-400-1	Oscillator--Single Phase	16067		2
	4800-400-2	Oscillator--Two Phase	16067		3
	4800-400-3	Oscillator--Three Phase	16067		4
	4810-201-7	Frame--Plug-in	16067	1-60001	7
	4800-200-1	Front Panel--Single Phase	16067	1-60072	9
	4800-201-1	Front Panel--2 $\phi$ and 3 $\phi$	16067	1-60073	10
	4800-209-3	Dial Plate	16067	1-10175	13
	4800-203-7	Bracket Freq. Pot	16067	1-60075	14
	4800-204-7	Dial Pointer	16067	1-60076	15
	4810-202-7*	Name Plate Freq. and Phase	16067	1-60000	17
					18
					19
		* SILKSCREEN FREQ., ACCURACY, and PHASE AS DEFINED ON ERA			20
A1	4800-700-1 (ex.	P.C. Assy--Basic Osc.	16067		21
A2	4800-701-1	P.C. Assy--Two Phase	16067		22
A2	4800-701-2	P.C. Assy--Three Phase	16067		23
A3	4800-702-1	Switch Assy--Freq. Contr.	16067		24
	B4810-701-7	Shield	16067	1-60115	25
	NW4-2816	Washer, Nylon	95987	2-10323	26
R1A + B	CIC 503	Pot Dual Gang 10K-10K	16067	5-70108	27
R2A + B	CLR303/11	Pot Dual Gang 5K-5K 5%	0000F	5-70109	28
					29
					30
	4800-011-1	Ball Drive 5:1 Ratio	16067	2-10000	31
	2374	Spacer 4-40 x 1 Long	83330	2-10025	32
	2340	Spacer #4 x 1/4 Lg.	83330	2-10026	34
					35
					36

MODEL 800T 20/20KHz 1% Any  $\phi$  CALICO P N 4800-400  
 TITLE PRECISION OSCILLATOR -- VAR. 3 BAND  
 SHT 3 OF 4  
 D ASSY R.O.







REF. DES.	MFG. NUMBER	DESCRIPTION	CODE IDENT	CALICO STOCK NUMBER	ITEM NO.
A1	4800-700-1	F. C. BOARD ASSEMBLY	16067		1
	-7	F. C. BOARD	16067	1-55066	2
	SEE SHEET 3	EYELET			3
					4
					5
A1C1	CK 502	CAPACITOR .005 $\mu$ F	71590	6-10078	6
A1C2	CM05E221J03	CAPACITOR 220 pF	04062	6-10036	7
A1C3	CM05E221J03	CAPACITOR 220 pF	04062	6-10036	8
A1C4	CK 502	CAPACITOR .005 $\mu$ F	71590	6-10078	9
A1C5	CM06F511J03	CAPACITOR 510 pF	04062	6-10046	10
A1C6	CM05E200J03	CAPACITOR 20 pF	04062	6-10008	11
A1C7	TE 1204	CAPACITOR 10 $\mu$ F 25V	56289	6-10338	12
A1C8	TE 1204	CAPACITOR 10 $\mu$ F 25V	56289	6-10338	13
A1C9	CM05E510J03	CAPACITOR 51 pF	JEDEC	6-10016	14
A1CR1	CIC 702-7	DIODE	16067	3-10189	16
A1CR2	IN 914	DIODE	JEDEC	3-10118	17
A1CR3	IN 914	DIODE	JEDEC	3-10118	18
A1CR4	IN 5226B	DIODE ZENER	04713	3-10129	19
A1CR5	IN 5245	DIODE ZENER	04713	3-10067	20
A1CR6	IN 5245	DIODE ZENER	04713	3-10067	21
A1CR7	IN 270	DIODE	93332	3-10006	22
A1CI1	CIC 200-2	I. C.	16067	3-30006	24
A1CI2	CIC 200-2	I. C.	16067	3-30006	25
A1CI3	CIC 200-2	I. C.	16067	3-30006	26
					27
					28
					29
A1O1	CIC 718	TRANSISTOR	16067	3-30041	30
A1O2	2N 2102	TRANSISTOR	95303	3-30075	31
A1O3	3N 138	MOS FET	95303	3-30136	32
A1O4	2N 4036	TRANSISTOR	95303	3-30106	33
					34
					35
					36

PARTS LIST

CODE IDENT 16067

CALIFORNIA INSTRUMENTS CORP. SAN DIEGO, CALIFORNIA



TITLE BASIC OSCILLATOR P. C. BOARD ASSEMBLY

MODEL 800T CALICO P.N. 4800-700

SHT 4 OF 5

A1 ASSY R.O. B









REF. DES.	MFG. NUMBER	DESCRIPTION	CODE IDENT	CALICO STOCK NUMBER	ITEM NO.
A2	4800-701-0	P.C. BOARD ASSY--Basic	16067		1
A2C2	4800-701-1	P.C. BOARD ASSY--Two φ	16067		2
A2C3	4800-701-2	P.C. BOARD ASSY--Three φ	16067		3
	-7	P.C. BOARD	16067	1-55067	4
	SEE SHEET 3	EYELET			5
A2C1	DD 501	CAPACITOR 500 pf	71590	6-10725	7
A2C2	CM05E390J03	CAPACITOR 39 pf	04062	6-10013	8
A2C3	DD501	CAPACITOR 500 pf	71590	6-10725	9
A2C4	DD501	CAPACITOR 500 pf	71590	6-10725	10
A2C5	DD501	CAPACITOR 500 pf	71590	6-10725	11
A2C6	CM05E510J03	CAPACITOR 51 pf	04062	6-10016	12
A2C7	CM05E390J03	CAPACITOR 39 pf	04062	6-10013	13
A2C8	CM05E390J03	CAPACITOR 39 pf	04062	6-10013	14
A2C9	t.b.d.	CAPACITOR			15
A2C10	t.b.d.	CAPACITOR			16
A2C11	CM05	CAPACITOR, T.B.D.	81349		17
A2IC1	CIC 200-1	INTEGRATED CIRCUIT	16067	3-30134	18
A2IC2	CIC 200-1	INTEGRATED CIRCUIT	16067	3-30134	19
A2IC3	CIC 200-1	INTEGRATED CIRCUIT	16067	3-30134	20
A2IC4	CIC 200-1	INTEGRATED CIRCUIT	16067	3-30134	21
					22
					23
					24
A2R1	RN60D2872F	RESISTOR 28.7K 1%	JDEDC	5-60107	25
A2R2	U-201R252	VAR. RESISTOR 2.5K	11237	5-70023	26
A2R3	RN60D1002F	RESISTOR 10K 1%	JDEDC	5-60079	27
A2R4A	S51-9-44.62K-.1%	RESISTOR 44.62K .1%	17745	5-80134	28
A2R4B	S51-9-51.52K-.1%	RESISTOR 51.52K .1%	17745	5-80136	29
A2R5A	S51-9-30.8K-.1%	RESISTOR 30.8K .1%	17745	5-80133	30
A2R5B	S51-9-68.87K-.1%	RESISTOR 68.87K .1%	17745	5-80137	31
A2R6	S51-9-50.0K-.1%	RESISTOR 50K .1%	17745	5-80135	32
A2R7	RC07GF332J	RESISTOR 3.3K 1/4W 5%	JDEDC	5-10064	33

MODEL 800T

CALICO P.N. 4800-701

TITLE SINGLE φ TO MULTI φ CONVERTER BOARD ASSEMBLY

OF 5

A2

ASSY R.O.

PARTS LIST

CODE IDENT 16067

CALIFORNIA INSTRUMENTS CORP. SAN DIEGO, CALIFORNIA





**PARTS LIST**

REF. DES.	MFG. NUMBER	DESCRIPTION	CODE IDENT	CALICO STOCK NUMBER	ITEM NO.
A2R8	S51-9-100.0K-.1%	RESISTOR 100K .1%	17745	5-80138	37
A2R9	RC07GF152J	RESISTOR 1.5K 1/4W 5%	JEDEC	5-10057	38
A2R10	RC07GF152J	RESISTOR 1.5K 1/4W 5%	JEDEC	5-10057	39
A2R11	S51-9-9.9K-.1%	RESISTOR 9.9K .1%	17745	5-80131	40
A2R12	RC07GF152J	RESISTOR 1.5K 1/4W 5%	JEDEC	5-10057	41
A2R13	RC07GF103J	RESISTOR 10K 1/4W 5%	JEDEC	5-10076	42
A2R14	S51-9-10.0K-.1%	RESISTOR 10K .1%	17745	5-80132	43
A2R15	S51-9-10.0K-.1%	RESISTOR 10K .1%	17745	5-80132	44
A2R16	RC07GF152J	RESISTOR 1.5K 1/4W 5%	JEDEC	5-10057	45
A2R17	RC07GF201J	RESISTOR 200Ω 1/4W 5%	JEDEC	5-10036	46
A2R18	RC07GF102J	RESISTOR 1K 1/4W 5%	JEDEC	5-10053	47
A2R19	U-201R251B	VAR. RESISTOR 250Ω	11237	5-70012	48
A2R20	RC07GF201J	RESISTOR 200Ω 1/4W 5%	JEDEC	5-10036	49
A2R21	RC07GF201J	RESISTOR 200Ω 1/4W 5%	JEDEC	5-10036	50
A2R22	RC07GF201J	RESISTOR 200Ω 1/4W 5%	JEDEC	5-10036	51
A2R23	RC07GF222J	RESISTOR 2.2K 1/4W 5%	JEDEC	5-10060	52
A2R24	RC07GF473J	RESISTOR 47K 1/4W 5%	JEDEC	5-10092	53
					54
					55
	#24 AWG	BUSS WIRE			56
					57



ITEM NO.	QTY	DESCRIPTION	CODE IDENT	STOCK NUMBER
1		SWITCH ASSEMBLY	16067	
2		P.C. BOARD	16067	1-55068
3		SEE SHEET 3		
4				
5				
6		ROTARY SWITCH-SPEC.	16067	2-40415
7				
8		CAPACITOR .11 $\mu$ F 100V		6-10151
9		CAPACITOR 1.0 $\mu$ F 25V		6-10152
10		CAPACITOR 5 $\mu$ F 50V		6-10331
11		CAPACITOR .11 $\mu$ F 100V		6-10131
12		CAPACITOR 2.0 $\mu$ F 25V		6-10325
13		CAPACITOR 10 $\mu$ F 50V		6-10330
14				
15				
16		VAR. RESISTOR 200 $\Omega$		5-70137
17		VAR. RESISTOR 200 $\Omega$		5-70137
18		RESISTOR 402 $\Omega$ 1%		5-60400
19				
20		VAR. RESISTOR 200 $\Omega$		5-70137
21		VAR. RESISTOR 200 $\Omega$		5-70137
22		RESISTOR 402 $\Omega$ 1%		5-60400
23		RESISTOR 270 $\Omega$ 1/4W 5%		5-10039
24				
25		CAPACITOR .0068 $\mu$ F 5%		6-10675
26		CAPACITOR		
27		CAPACITOR .0075 $\mu$ F 5%		6-10676
28		CAPACITOR .068 $\mu$ F 5%		6-10677
29		CAPACITOR .075 $\mu$ F 5%		6-10678
30		CAPACITOR .68 $\mu$ F 5%		6-10679
31		CAPACITOR		
32		CAPACITOR		
33		CAPACITOR .0068 $\mu$ F 5%		6-10675
34		CAPACITOR		
35		CAPACITOR .0075 $\mu$ F 5%		6-10676
36		CAPACITOR .068 $\mu$ F 5%		6-10677
37				
38		6MPD-2-752J		
39		PDA683 F.5%		
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PARTS LIST





ITEM NO.	M.F.C. NUMBER	DESCRIPTION	CODE IDENT	STOCK NUMBER	QTY	UNIT	PRICE	TOTAL
37	AMPD-3-753J	CAPACITOR .075 $\mu$ F 5%	72135	6-10678				
38	PDSA684 F. 5%	CAPACITOR .68 $\mu$ F 5%	12406	6-10679				
39	F. P. D.	CAPACITOR						
40								
41								
42	#24 AWG	BUSS WIRE						
43	for #24 GA	TEFLON SLEEVING						
44	for #20AWG	TEFLON SLEEVING						







## ONE YEAR WARRANTY

CALIFORNIA INSTRUMENTS COMPANY a division of AIKEN INDUSTRIES INC. warrants each instrument manufactured by them to be free from defects in material and workmanship for a period of one year from the date of shipment to the original purchaser. Excepted from this warranty are tubes, fuses, and batteries which carry the warranty of their original manufacturer where applicable. CALIFORNIA INSTRUMENTS COMPANY will service, replace, or adjust any defective part or parts, free of charge, when the instrument is returned to the Company freight prepaid, and when examination reveals that the fault has not occurred because of misuse or abnormal conditions of operation. Equipment repaired beyond the effective date of warranty or when abnormal usage has occurred will be charged at applicable rates. CALIFORNIA INSTRUMENTS COMPANY will submit an estimate for such charges before commencing repair if so requested.

## PROCEDURE FOR SERVICE

If a fault develops, notify CALIFORNIA INSTRUMENTS COMPANY or its local representative, giving full details of the difficulty, and including the model number and serial number. On receipt of this information, service data or shipping instructions will be furnished. If shipment is indicated, forward the instrument, freight prepaid, to the authorized repair station indicated in the instruction. If requested, an estimate of the charges will be made before the work begins, provided the instrument is not covered by the warranty.

## DAMAGE IN TRANSIT

The instrument should be tested as soon as it is received. If it fails to operate properly, or is damaged in any way, a claim should be filed with the carrier. A full report of the damage should be obtained by the claim agent, and this report should be forwarded to us. CALIFORNIA INSTRUMENTS COMPANY will advise the disposition to be made of the equipment and arrange for repair or replacement. Please include model number and serial number when referring to the instrument.

